



**Bharatiya Vidya Bhavan's**  
**SARDAR PATEL INSTITUTE OF TECHNOLOGY**  
An Autonomous Institute Affiliated To University Of Mumbai  
Munshi Nagar, Andheri (W) Mumbai 400 058

**Gazette for TY.B.Tech Electronics & Telecommunication Engineering Even Semester VI Regular (Internship) Examination held in July 2024**

																EC310										
																Research Internship										
Sr. No.	UID	Name	Class	PE I name of course	marks	Grade	Credits Earned	grade point	GP*C	PE 2 name of course	Marks	Grade	Credits Earned	grade point	GP*C	Marks	Grade	Credits Earned	grade point	GP*C	TOTAL COURSE CREDITS	Credits Earned	Total C*GP	SEM VI SGPA	CGPA	Remark
1	2021200012	Ansh Bhansali	EXTC A	Embedded Systems Design	52	BC	3	7	21	Industrial automation and control	55	BC	3	7	21	77.00	AB	15	9	135	21	21	177	8.43	8.92	Successful
2	2021200017	Bhavisha Chafekar	EXTC A	Smart Grid: Basics to Advanced Technologies	80	AB	3	9	27	Communication Networks	75	AA	3	10	30	78.00	AB	15	9	135	21	21	192	9.14	7.78	Successful
3	2021200021	Kunj chauhan	EXTC A	Introduction to Embedded System Design	70	AA	3	10	30	Industrial Automation And Control	49	BC	3	7	21	93.00	AA	15	10	150	21	21	201	9.57	7.94	Successful
4	2021200026	Kris Dcosta	EXTC A	Smart Grid: Basics to Advanced Technologies	84	AA	3	10	30	Communication Networks	72	BB	3	8	24	92.00	AA	15	10	150	21	21	204	9.71	9.02	Successful
5	2021200027	Niranjan Devadiga	EXTC A	Computer Vision And Image Processing - Fundamentals And Applications	49	BC	3	7	21	Communication Networks	56	BC	3	7	21	90.00	AA	15	10	150	21	21	192	9.14	7.73	Successful
6	2021200033	Shreejit Gherde	EXTC A	Design and Analysis of VLSI Subsystems	52	BC	3	7	21	VLSI Physical Design with Timing Analysis	49	FF	0	0	0	86.00	AA	15	10	150	21	18	171	-	-	Unsuccessful
7	2021200045	Raghavendra Joshi	EXTC A	Photonic integrated circuit	60	BB	3	8	24	VLSI Physical Design with Timing Analysis	49	BC	3	7	21	89.00	AA	15	10	150	21	21	195	9.29	7.83	Successful
8	2021200058	Esha Khot	EXTC A	Embedded Systems Design	58	BC	3	7	21	Introduction to Embedded System Design	68	AA	3	10	30	86.00	AA	15	10	150	21	21	201	9.57	8.06	Successful
9	2021200062	Sahil Kurkure	EXTC A	Embedded Systems Design	72	AA	3	10	30	Introduction to Embedded System Design	75	AA	3	10	30	94.00	AA	15	10	150	21	21	210	10.00	8.95	Successful
10	2021200063	Saket Vijay Lad	EXTC A	Smart Grid: Basics to Advanced Technologies	72	BB	3	8	24	Communication Networks	75	AA	3	10	30	92.00	AA	15	10	150	21	21	204	9.71	8.48	Successful
11	2021200083	Preyes Parab	EXTC B	Introduction to Embedded System Design	76	AA	3	10	30	ision And Image Processing - Fundamentals And Applications	70	AA	3	10	30	94.00	AA	15	10	150	21	21	210	10.00	9.00	Successful
12	2021200087	Neel patel	EXTC B	Introduction to Embedded System Design	83	AA	3	10	30	ision And Image Processing - Fundamentals And Applications	79	AA	3	10	30	89.00	AA	15	10	150	21	21	210	10.00	9.00	Successful
13	2021200088	Om Patil	EXTC B	Introduction to Embedded System Design	78	AA	3	10	30	ision And Image Processing - Fundamentals And Applications	63	BB	3	8	24	87.00	AA	15	10	150	21	21	204	9.71	8.83	Successful
14	2021200093	Malay Phadke	EXTC B	Introduction to Embedded System Design	83	AA	3	10	30	ision And Image Processing - Fundamentals And Applications	77	AA	3	10	30	81.00	AB	15	9	135	21	21	195	9.29	9.50	Successful
15	2021200094	Mayuresh Pitale	EXTC B	Embedded Systems Design	51	BC	3	7	21	Introduction to Embedded System Design	66	BB	3	8	24	95.00	AA	15	10	150	21	21	195	9.29	7.40	Successful

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Principal



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16	2022201020	Ansh Singhal	EXTC B	Computer Vision And Image Processing - Fundamentals And Applications	44	BC	3	7	21	Communication Networks	51	FF	0	0	0	84.00	AB	15	9	135	21	18	156	-	-	Unsuccessful

Grade	Points	Definition
FF	0	All the students who have scored below half the median of the class or have adopted unfair means will be declared failure in the course. (< M/2, where M is the <b>median of the class</b> )
AA	10	All the students who have scores <b>more than SA</b> will receive the AA. (Top Grade)
AB (SA)	9	The range between SA and M/2 will be divided in <b>6 equal intervals</b> and will be allotted these grades chronologically. <b>The bottom limits</b> for these grades will be as follows:
		M/2.....DD
BB	8	M/2+1.....CD
BC	7	M/2+12.....CC
CC	6	M/2+13.....BC
CD	5	M/2+14.....BB
DD	4	M/2+15.....AB
X	0	Defaulter
NP	0	Absent
PN	-	pending
NR	-	not registered

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