



( Founded in 1938 by Kulapati Dr. K. M. Munshi with the blessings of Mahatma Gandhi)

आ नो भद्रा : क्रतवो यन्तु विश्वतः।

Let noble thoughts come to us from every side

## **SARDAR PATEL INSTITUTE OF TECHNOLOGY** (Autonomous Institute)

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai - 400 058, India  
E mail: [principal@spit.ac.in](mailto:principal@spit.ac.in) Website: [www.spit.ac.in](http://www.spit.ac.in)

### **Recruitment of Technical Project Staff**

Applications are invited from the Indian nationals for the following positions to work on a Government of India funded Technology Development Project titled "Design and development of a fully automated pressure and vacuum controlled Induction Casting Machine" (Sanction No. DST/TDT/TDP-16/2022, dated 11-04-2023) under Department of Science and Technology (DST) for three years. The recruitment terms and conditions shall be as per DST Norms: (OM No. SR/S9/Z-08/2018 dated 30.01.2019 and SR/S9/Z-05/2019 dated 10.07.2020).

#### **Position-1:**

- (1) Name of the Position: **Project Associate-1**
- (2) Number of Positions: 01
- (3) Last date of Application: **10<sup>th</sup> July 2023**
- (4) Duration of the Position: Two Years, Extended for One more year
- (5) Essential Educational Qualifications: BE/B Tech in Electrical/Electronics/Electronics & Telecom/Instrumentation with minimum Valid GATE Exam Score **OR** M E / M Tech Appearing/Completed in Electrical/Electronics/Electronics & Telecom/Instrumentation from reputed university with Valid GATE Score
- (5) Emoluments: Rs. 31000/- +HRA Rs. 4000 per month for a Candidate having **Valid GATE Score**  
**OR** Emoluments: Rs. 25000/- +HRA Rs. 3250 per month for a Candidate **without GATE Score**
- (6) Upper Age Limit: 35 Years

#### **Position-2:**

- (1) Name of the Position: **Technical Assistant**
- (2) Number of Positions: 01
- (3) Last date of Application: **10<sup>th</sup> July 2023**
- (4) Duration of the Position: Two Years, Extended for One more year
- (5) Emoluments: Rs. 20,000/- per month
- (6) Essential Educational Qualifications: Diploma in Electrical/Industrial Electronics/Mechanical/Instrumentation/Electronics
- (7) Upper Age Limit: 50 Years

**Application Procedure:** Make a single PDF file (Application form along with self-attested all relevant qualification certificates, mark sheets, experience certificate, age proof, and any other documents in order) and email it to [rajendra.sawant@spit.ac.in](mailto:rajendra.sawant@spit.ac.in) with subject line as **Application for -----, DST/TDP/ SPIT, Mumbai**. There is no need to send the hard copy. However, candidates need to show the original documents at the time of interview as well as the time of joining.

**(For skilled and experienced candidate higher salary may be offered)**

Date: April 2023

M Tech Students, who have completed their course-work may be encouraged to link their dissertation work with the current research project, if selected.

Dr. Rajendra R Sawant,  
Principal Investigator of the DST Project DST/TDT/TDP-16/2022  
Professor, Department of Electronics and Telecommunication,  
Sardar Patel Institute of Technology  
Munshi Nagar, Andheri West, Mumbai-400058

## Desirable Skills Specific to the Project:

The execute the current DST sponsored project we need certain skill-sets to be possessed by the Candidate. Here is the listing our specific requirements:

### Position-1

- (1) The knowledge of electronic/electrical integrated circuits OR should be able to infer the relevant design information from the datasheets
  - (2) The exposure to drawing circuit schematics and PCB design on any of the platforms like Eagle/KiCad/Cadence-OrCad/Altium etc.
  - (3) The system level design of Mixed Mode circuits including Micro-controllera/DSP/FPGA etc and Embedded Controllers
  - (4) The knowledge of Embedded Coding for firmware developments in C/C++/Python, etc.
  - (5) The experience of independently working on electronic products and instruments
  - (6) The knowledge of Quality Checking, testing and debugging the electronic hardware
  - (7) The Knowledge of Technical Documentation for Technology Transfer
  - (8) The experience of drafting reports and making presentations
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### Position-2

- (1) The experience of assembly, Soldering and testing of Electronics Circuit board as per the test procedure
  - (2) The knowledge and experience of drafting electrical wiring and mechanical assembly on AutoCAD or any other CAD software
  - (3) The experience of Panel wiring and mechanical fittings
  - (4) The experience on handling pressure valves and Vacuum control systems
  - (5) The experience on handling electrical switchgear
  - (6) The Experience of turning, fitting, welding and other machines used in the workshop
  - (7) Some exposure to 3D-Printing/scanning, laser cutting machines and tools
  - (8) Some exposure to handling of SMT Machine, Reflow Ovens, Wave soldering machines, pick-n-place machines, Automated Optical Inspection Machines, etc
  - (9) Some exposure to high power Furnaces and Heaters
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## APPLICATION FOR PA-1/TA POSITION

(Fill the form in handwriting and in capital letters)

1. Full Name:

2. Date of Birth:

3. Sex: M/F/Other

4. Category: GEN/SC/ST/OBC/EW/Other

5. Marital Status:

6. Nationality:

7. Divyaang: Y/N

8. Father's Name:

9. Mother's Name

10. Address of Correspondence:

11. Phone Number:

12. Email-id:

13. Academic Record:

13.a. Gate Score

13.b AIR Gate Rank

13.c. Qualifying Degree

Qualification	University/Institute/Board	Passing Year	Branch	%/CGPA	Division

14. Details of Professional/ Work Experience:

Organization	Designation	Date-From	Date-To

15. Attach a separate list of publications with clear mention of 2021 impact factor strictly in IEEE style (if any).

16. Details of three Referees:

Name	Affiliation	Contact Number	Email ID	Relation with Applicant

**Declaration:** I hereby declare that I have carefully read and understood the instructions and particulars on this application and that all entries in this form as well as in the attached sheets are true to the best of my knowledge and belief.

Date:

Signature

Place:

\*Incomplete application will be rejected and only shortlisted candidates will be intimated by email.