ZARGUN ELECTRONICS & SCIENCE (OPC) PVT. LTD.

CIN: U73200MH2019 OPC320315

Vaishali Nagar CHS. Ltd. B-3/104, Vaishali Nagar, S.V. Road, Oshiwara, Jogeshwari, Mumbai City, Maharashtra, India, 400102.

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Memorandum of Understanding (MOU)

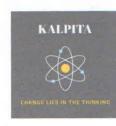
Zargun Electronics & Science Pvt. Ltd. [CIN: U73200MH2019OPC320315]

Background and credentials

Zargun Electronics and Science was formed in Jan 2019 with an objective to solve real-world issues like waste disposal, water management, etc. by leveraging technology and collaborating with stakeholders.

Currently, it is actively involved in consulting startups, private and public sector for adopting cost-effective solutions. Also, it is developing customized products to meet end-user requirements





Zargun Electronics & Science Pvt.Ltd. is founded by Mr. Gurudatt Rao, an SP Alumni with Masters (MS) in Electrical & Computer Engineering (ECE) from Ohio State University (OSU), Columbus, USA & Master's degree (M. Tech) in Materials Science (MSE) from IIT Bombay. He has worked with companies like Siemens India, TSMC and EPISTAR in areas like Advanced Technology Nodes for System on Chip (SoC) development, Research on non-charge-based alternatives to replace flash memory and SSD, Improving the efficacy of Light-emitting Diode (LED) & power devices like High Electron Mobility Transistor (HEMT), Evaluating III-Nitride (Compound Semiconductor) for linearity and High-Temperature Electronics.

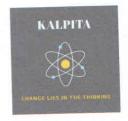
At present, Mr. Gurudatt Rao holds membership(corporate) to societies like IEEE, IETE, and IEI, India.

Background of Sardar Patel Institute of Technology

Sardar Patel Institute of Technology (S.P.I.T.) is an AICTE recognized college spread over a campus of 47 acres and is affiliated to the Mumbai University. It imparts various degree courses in Engineering and also certificates courses. SPIT aspires to be one of the premier R&D organizations in the academic world.

It is also involved in Research & Development in the area of Embedded Systems, VLSI design, Power Electronics, Software Technology and related areas of computer science. Its focus is to help create cutting-edge Technologies and offer advanced training for students, Government and Industry.





Alliance Objective

The scope of the MoU, the roles and responsibilities of the parties of the MoU are given below

- Zargun Electronics & Science (OPC) Pvt. Ltd. agrees to offer a paid internship to the student community of SPIT
- Zargun Electronics & Science (OPC) Pvt. Ltd. will provide a Letter of Recommendation (LOR) to eligible students on successful completion of the internship
- The product development initiative will primarily be in the domain (but not exclusively)
 in Embedded Systems Design, Instrumentation & Automation, Power Electronics,
 Networking, and Communication
- 4. SPIT will provide infrastructure & laboratory access (24 x 7) to all the team members (including interns) by issuing ID cards
- 5. This Alliance will assist SPIT to enhance its industry collaboration portfolio

Implementation Plan

Zargun Electronics & Science (OPC) Pvt. Ltd. will be working in the Product Development Cell(PDC) of SPIT (Sardar Patel Institute of Technology).

The duration of the project is nine months (Oct 3, 2019 – July 3, 2020). Extension/Renewal of the same will be subject to ratification by both the parties

The project cost estimate is Rs. 3,00,000 (Exclusive of GST)





The project layout will be as follows:

Sr. No.	Quarter	Estimated Cost(INR)			Amount (INR)	Comments
		To SPIT (Fixed)	Components & Peripheral	HR Cost		
1	Q1(Oct'19-Jan'20)	30000	35000	35000	100000	GST not included
2	Q2(Jan'20-April'20)	30000	30000	40000	100000	GST not included
3	Q3(April'20-July'20)	30000	25000	45000	100000	GST not included

The fixed charges to SPIT will be released in installments of Rs. 30,000 (Exclusive of GST) on the 1st week of respective quarter.

Human Resource(HR) for the project will be selected as follows:

- 1) The students of SPIT will be initially hired as part-time interns till January 2020 and full-time interns from January-June, 2020 subject to satisfactory performance. Selection of the eligible candidates will be done via shortlisting and the interview process through the Training & Placement Office (TPO), Sardar Patel Institute of Technology, if feasible
- 2) Subject matter experts/mentors, interns, additional hirees can be brought onboard inline with the requirement of the project. Their charges would be attributable in the HR cost
- 3) Final Selection of interns/experts & Payment terms will be the sole discretion of Zargun Electronics & Science(OPC) Pvt. Ltd.

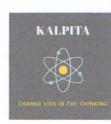




Mutual Obligation

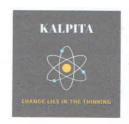
- 1. This MoU may be terminated by either party with prior notice of 30 days
- 2. Either party may terminate this MoU if the implementation of the said clauses is not adhered to
- There shall be no liability on any party which might arise from the termination of this MoU
- 4. This agreement may not be amended without the prior written consent of both the parties
- 5. Neither party shall issue a press statement, make a public announcement or other such disclosure concerning the agreement till it is mutually consented upon
- 6. "Confidential Information" means any and all information and knowledge regarding the project ,but not limited, to Zargun Electronics & Science (OPC) Pvt. Ltd. proprietary methods and methodology, technical data, trade secrets, know-how, research and development information, product plans, products, services, client information of both existing and prospective clients, corporate relationships, suppliers, markets, computer and electronic software, computer software development, inventions, Processes, formulas,technology, designs, drawings, technical information, source codes, engineering information, hardware configuration information, and matters of a business nature such as information regarding costs, pricing, finances, financial models and projections, billings or other similar business information will be strictly kept confidential and will be the sole property of Zargun Electronics & Science (OPC) Pvt. Ltd.
- Intellectual Property Rights: IPR titles, commercialization rights or ownership of any
 products, blueprints, hardware designs, proprietary information or technology tools,
 processes, utilities, and methodology including any Zargun Electronics & Science (OPC)





Pvt. Ltd. proprietary products or components thereof used hereunder or development of any deliverables and all ideas, inventions, innovations, or developments conceived as a part of any work under this MoU will solely belong to Zargun Electronics & Science (OPC) Pvt. Ltd. and not be transferred or deemed to be transferred from Zargun Electronics & Science (OPC) Pvt. Ltd. on account of the use of the same and shall always remain with Zargun Electronics & Science (OPC) Pvt. Ltd.





Summary:

Zargun Electronics & Science (OPC) Pvt. Ltd. recognizes the significance of the S.P.I.T. initiative to be the leader in the field of Electronics, Communication and Computer-related academia in the country. Accordingly, the company proposes to assist SPIT to maintain its position as a leading institute in the country by mentoring its student talent pool through an internship program.

This Memorandum of Understanding(MOU) is intended to express the broad understanding of the parties regarding their working with each other to the extent possible for their mutual benefit.

In written whereof both parties put their hard seal on the day, month and year herein mentioned.

Date:

Principal

Sardar Patel Institute of Technology,

Bhavans Campus, Andheri (West)

Mumbai-58.

E-mail: principal@spit.ac.in

Telephone: (022) 26708520 Ext: 305

Signature:

Date:

Zargun Electronics & Science Pvt. Ltd

B-3/104, Star Vaishali, Vaishali Nagar,

Off S.V. Road, Jogeshwari (W),

Mumbai-102

E-mail: gurudattrao@zargunes.com

Telephone: +91-877-973-4490

Signature:









