



SARDAR PATEL INSTITUTE OF TECHNOLOGY
Munshi Nagar, Andheri (W) Mumbai 400 058
 (Affiliated to University of Mumbai)

Centre: 735, SPIT

Office register for S.E., Electronics Engineering, Semester IV (CBGS), Exam: First Half December 2020 (A.T.K.T)

Courses →	Applied Mathematics IV	EXS401			EXC402			EXC403			EXC404			EXC405			EXC406			EXL401			EXL402			EXL403			EXL404			TOTAL	SGPI (GPA)	RESULT				
		ESE	IA	TOT	TW	ESE	IA	TOT	ESE	IA	TOT	ESE	IA	TOT	ESE	IA	TOT	ESE	IA	TOT	PR	OR	TW	TOT	OR	TW	TOT	OR	TW	TOT	PR				OR	TW	TOT	
Seat No / Name of Student ↓	MaxM	80	20	100	25	80	20	100	80	20	100	80	20	100	80	20	100	60	15	75	50	25	75	25	25	50	25	25	50	25	25	50	50	25	75	850		
	MinM	32	8	40	10	32	8	40	32	8	40	32	8	40	32	8	40	24	6	30	20	10	30	10	10	20	10	10	20	20	10	30						
24101	MarksO	58	14+	72	12+	64	08+	72	32+	10+	42	32+	09+	41	32+	13+	45	36+	10+	46	35+	14+	49	17+	13+	30	14+	15+	29	37+	15+	52	490					
GITE PRASAD SHRIPAD SANGITA	Grade	B	B	B	E	O	P	B	P	D	P	P	E	P	P	C	E	C	C	C	B	D	C	C	D	C	D	C	D	B	C	C						
	C			4	1			4			4			4			4			3			1			1			1			1	28					
	GP*C			32	5			32			16			16			20			21			7			7			6			7	169	6.04	P			

Female	/
0.229	#
0.5042	@
0.5045	*
3.4CBGSM	!
0.5050	RCC
Pass	P
Fail	F
Exempted	E
Reserved for Lower Exam	RLE
Dyslexia Benefit	~

Credit	C
Grade	G
Grade Point	GP
C*GP	CGP
Sum of Credits	ΣC
Sum of CGPs	ΣCGP
ΣCGP/ΣC	GPA
Null & Void	NULL
Absent	Ab

% Marks	Grade	GP
M>=80	O	10
75>=M<80	A	9
70>=M<75	B	8
60>=M<70	C	7
50>=M<60	D	6
45>=M<50	E	5
40>=M<45	P	4
M<40	F	0

Entered by

Checked by

Verified By

Principal



Principal
 Sardar Patel Institute of Technology
 Bhavan's Campus,
 Munshi Nagar, Andheri (West)
 Mumbai - 400 058