



SARDAR PATEL INSTITUTE OF TECHNOLOGY
Munshi Nagar, Andheri (W) Mumbai 400 058
 (Affiliated to University of Mumbai)

Centre: 735, SPIT

Office Register for S.E., Electronics and Telecommunication Engineering, Semester III (CBGS), Exam: First Half December 2020 (A.T.K.T)

Courses →	ETS301				ETC302			ETC303			ETC304			ETC305			ETL301			ETL302			ETL303			ETSL304			TOTAL	SGPI (GPA)	RESULT	
	Applied Mathematics III				Analog Electronics I			Digital Electronics			Circuits and Transmission Lines			Electronic Instruments and Measurements			Analog Electronics I Laboratory			Digital Electronics Laboratory			Circuits and Measurements Laboratory			Object Oriented Programming Methodology Laboratory						
Seat No / Name of Student ↓	ESE	IA	TOT	TW	ESE	IA	TOT	ESE	IA	TOT	ESE	IA	TOT	ESE	IA	TOT	PR OR	TW	TOT	PR OR	TW	TOT	TW			PR OR	TW	TOT	725			
	MaxM	80	20	100	25	80	20	100	80	20	100	80	20	100	80	20	100	25	25	50	25	25	50	25			50	25				75
	MinM	32	8	40	10	32	8	40	32	8	40	32	8	40	32	8	40	10	10	20	10	10	20	10			20	10				30
23401	MarksO	42	09+	51	10+	41+	08+	49	32+	10+	42	32+	08+	40	32+	08+	40	16+	20+	36	12+	18+	30	18+			26+	16+	42	358	5.04	P
KOLI VINIT SUDAM SUNITA	Grade	D	E	D	P	D	P	E	P	D	P	P	P	P	P	P	C	O	B	E	B	C	B			D	C	D				
	C			4	1			4			4			4			4			1			1	1					2	26		
	GP*C			24	4			20			16			16			16			8			7	8					12	131		

Female	/
0.229	#
0.5042	@
0.5045	*
3.4CBGSM	!
0.5050	RCC
Pass	P
Fail	F
Exempted	E
Reserved for Lower Exam	RLE
Dyslexia Benefit	~

Credit	C
Grade	G
Grade Point	GP
C*GP	CGP
Sum of Credits	ΣC
Sum of CGPs	ΣCGP
ΣCGP/ΣC	GPA
Null & Void	NULL
Absent	Ab

% Marks	Grade	GP
M>=80	O	10
75>=M<80	A	9
70>=M<75	B	8
60>=M<70	C	7
50>=M<60	D	6
45>=M<50	E	5
40>=M<45	P	4
M<40	F	0

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Verified By

[Signature]
Principal



Principal
 Sardar Patel Institute of Technology
 Bhavan's Campus,
 Munshi Nagar, Andheri (West)
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