



UNIVERSITY OF MUMBAI
SARDAR PATEL INSTITUTE OF TECHNOLOGY
MUNSHI NAGAR, ANDHERI(WEST), MUMBAI - 400058

OFFICE REGISTER FOR THE SECOND YEAR ENGINEERING: ELECTRONICS AND TELECOMMUNICATION ENGINEERING SEMESTER IV (OTR) EXAMINATION HELD IN FIRST HALF DECEMBER 2020

SEAT NO	STUDENT NAME	APPLIED MATHEMATICS IV	INTEGRATED CIRCUITS				ANALOG COMMUNICATION			ANALOG ELECTRONICS II				WAVE THEORY AND PROPAGATION		SIGNALS & SYSTEMS		SEM IV TOTAL	SEM III TOTAL	GRAND TOTAL	REMARKS
		1	2				3			4				5		6					
		TH	TH	TW	OR	PR	TH	TW	OR	TH	TW	OR	PR	TH	TW	TH	TW				
		40/100	40/100	10/25	10/25	10/25	40/100	10/25	10/25	40/100	10/25	10/25	10/25	40/100	10/25	40/100	10/25				
24OTR401	THAKUR RANDHIR SUSHIL KUMAR	AB	AB	EX	EX	EX	AB	EX	EX	AB	EX	EX	EX	AB	EX	AB	EX	0	0	0	AB

RRLE: Result Reserved for Lower Examination Not Cleared /-Female Candidate AB : Absent + : Carried Forward @ : Under 0.5042/0.5043 * : Under 0.5045 # : Under 0.229 Z: Remarks E : Exemption X : Cleared with Exemption


ENTERED BY :


CHECKED BY :


VERIFIED BY :


PRINCIPAL :

Principal
 Sardar Patel Institute of Technology
 Bhavan's Campus,
 Munshi Nagar, Andheri (West)
 Mumbai - 400 058

