



UNIVERSITY OF MUMBAI
SARDAR PATEL INSTITUTE OF TECHNOLOGY
MUNSHI NAGAR, ANDHERI(WEST), MUMBAI - 400058

OFFICE REGISTER FOR THE SECOND YEAR ENGINEERING: ELECTRONICS AND TELECOMMUNICATION ENGINEERING SEMESTER III (OTR) EXAMINATION HELD IN FIRST HALF DECEMBER 2020

Seat No.	Name of the student	APPLIED MATHEMATICS III	DIGITAL ELECTRONICS		ANALOG ELECTRONICS I		CIRCUITS AND TRANSMISSION LINE		NUMERICAL METHODS & OPTIMISATION TECHNIQUES		PRINCIPLES OF CONTROL SYSTEMS		TOTAL	REMARKS
		1	2		3		4		5		6			
		TH	TH	TW	TH	TW	TH	TW	TH	TW	TH	TW		
		40/100	40/100	10/25	40/100	10/25	40/100	10/25	40/100	10/25	40/100	10/25		
23OTR401	THAKUR RANDHIR SUSHIL KUMAR	AB	AB	EX	AB	EX	AB	EX	AB	EX	EX	EX	0	AB

RRLE: Result Reserved for Lower Examination Not Cleared /-Female Candidate AB : Absent + : Carried Forward @ : Under 0.5042/0.5043 * : Under 0.5045 # : Under 0.229 Z: Remarks E : Exemption X : Cleared with Exemption

ENTERED BY :

CHECKED BY :

VERIFIED BY :

PRINCIPAL :



Principal
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