

**BHARATIYA VIDYA BHAVAN'S  
SARDAR PATEL INSTITUTE OF TECHNOLOGY  
MUNSHI NAGAR ANDHERI (W), MUMBAI - 400 058.  
EXAM - MSE ODD SEM, September 2019  
PROVISIONAL RESULT AFTER GRIEVANCE**

**SEM -V**                      **Date: 17/10/2019**

**Programme - B.Tech. Electronics & Telecommunication**

Sr No	UID NO	Last Name	Digital Communication	Microprocessor and Microcontroller	Fundamentals of Antenna	Random Signal Analysis
			20	20	20	20
1	2017120005	BANDI HARIT DINESH	-	13	10	-
2	2017120007	CHAUDHARI AJAY BHARAT	-	13	-	-
3	2017120015	GUPTA ABHISHEK KANHAIYALAL	-	17	-	-
4	2017120016	GUPTA RUCHI RAJESH	-	14	10	-
5	2017120021	JAIN MANAV SURENDAR	-	-	-	17.5
6	2017120023	JOSHI SUYASH SUKHENDU	-	15	-	-
7	2017120034	LINGAYAT APURVA SATISH	-	-	-	14.5
8	2017120038	MANDANI KASHISH CHETAN	-	5	16	-
9	2017120042	PAI PRATIK MAHESH	11	-	-	-
10	2017120046	PATOLE MEHUL SHRIKANT	-	14	-	15.5
11	2017120047	PINGE SOHAM NITIN	-	14	-	-
12	2017120049	RAMINA VISHAL KANARAM	-	15	15.5	-
13	2017120051	RAUT CHINMAY PRALHAD	-	8	-	-
14	2017120055	SHINGALA PARTH BHAGVANBHAI	-	13	-	-
15	2017120058	SINGH AYUSH DEVENDRA KUMAR SINGH	-	-	11	-
16	2017120060	SOHNI HARSH NILESH	11	-	-	-
17	2017120061	TAGDE OSHIN BHAGYAWAN	-	-	9	14.5
18	2017120063	YADAHALLI SRUSHTI SANJEEVA	-	-	-	13
19	2018220065	CHAVAN VAISHNAVI RAJENDRA	-	15	-	-
20	2018220067	DOGRA UNNATI RAJESH	-	12	-	-

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			<b>20</b>	<b>20</b>	<b>20</b>	<b>20</b>
21	2018220073	PANDYA ASHUTOSH KETAN	12	-	3	4
22	2018220074	SHAH RAVISH RAKESH	10	-	7	5
23	2017110016/ 2017320077	GADKAR MIHIR VINAYAK	-	-	-	7
24	2016120056	SONAWANE YUGMESH JAYWANTRAO	8	10	9	-
25	2016120025	KHANDELWAL DRUV	-	8	-	-

Controller of Examination  
sd/-

Principal  
sd/-