



**Bharatiya Vidya Bhavan's**  
**SARDAR PATEL INSTITUTE OF TECHNOLOGY**  
 An Autonomous Institute Affiliated To University Of Mumbai  
 Munshi Nagar, Andheri (W) Mumbai 400 058

Centre: 735, SPIT

**Gazette for Electronics Engineering (GROUP II 2018-2019), Odd Semester V , Exam: July 2019 (Re - Examination) (Batch 2018-19)**

Courses →	EL51				EL52				EL53				EL54				ELL51			ELL52			ELL53		ELL55		ELP56			MEC4		ABL3 @		TOTAL	SGPI (GPA)	RESULT		
	LINEAR INTEGRATED CIRCUITS				MICRO-ARCHITECTURES				SIGNALS AND SYSTEMS				ELECTROMAGNETIC ENGINEERING				LINEAR INTEGRATED CIRCUITS LAB			MICRO-ARCHITECTURES LAB			SIGNALS AND SYSTEMS LAB		INSTRUMENTATION LAB		ACTION RESEARCH PROJECT I			LAW FOR ENGINEERS		CREATIVE THINKING, DIVERSITY AND WORKPLACE ETIQUETTE (NONCREDIT)						
Seat No / Name of Student ↓	ISE	MSE	ESE	TOT	ISE	MSE	ESE	TOT	ISE	MSE	ESE	TOT	ISE	MSE	ESE	TOT	ISE	ESE	TOT	ISE	ESE	TOT	ISE	TOT	ISE	TOT	ISE	TOT	ISE	ESE	TOT	ISE	TOT	ESE	TOT			
	MaxM	20	20	60	100	20	20	60	100	20	20	60	100	20	20	60	100	40	20	60	40	20	60	40	40	40	40	40	40	40	20	60	50	50	0	0	710	
MinM	--	--	24	40	--	--	24	40	--	--	24	40	--	--	24	40	16	8	24	16	8	24	16	16	16	16	16	16	16	8	24	20	20	--	0			
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RAM G PRANAV	Grade			P				F				F				F			C			C					B			P		O		S				
	C			4				0				0				0			1			1					1		2		1		2		0	12		
	GP*C			16				0				0				0			7			7					7		16		4		20		0	77	-	F

Female	/
Grace	*
LD	!
0.505	RCC
Pass	P
Fail	F
Exempted	E
Reserved for Lower Exam	RLE

Credit	C
Grade	G
Grade Point	GP
C*GP	CGP
Sum of Credits	ΣC
Sum of CGPs	ΣCGP
ΣCGP/ΣC	GPA
Null & Void	NULL
Absent	Ab

Grade table			
% Marks	Grade	GP	Performance
80.00 and Above	O	10	Outstanding
75.00 - 79.99	A	9	Excellent
70.00 - 74.99	B	8	Very Good
60.00 - 69.99	C	7	Good
50.00 - 59.99	D	6	Fair
45.00 - 49.99	E	5	Average
40.00 - 44.99	P	4	Pass
Less than 40.00	F	0	Fail
--	X	0	Defaulter
--	AB	0	Absent

Grade table with penalty			
% Marks	Grade	GP	Performance
75.00 and Above	B	8	Very Good
70.00 - 74.99	C	7	Good
60.00 - 69.99	D	6	Fair
50.00 - 59.99	E	5	Average
40.00 - 49.99	P	4	Pass
Less than 40.00	F	0	Fail
--	X	0	Defaulter
--	AB	0	Absent

Entered by \_\_\_\_\_ Checked by \_\_\_\_\_ Verified By \_\_\_\_\_ Principal \_\_\_\_\_