

<b>BHARATIYA VIDYA BHAVAN'S SARDAR PATEL INSTITUTE OF TECHNOLOGY MUNSHI NAGAR ANDHERI (W), MUMBAI - 400 058.</b>						
<b>PROVISIONAL RESULT FOR ODD SEM RE EXAM EXAM JULY 2019</b>						
<b>Department - Electronics Engineering</b>			<b>SEM- V</b>		<b>Date:16/07/2019</b>	
<b>Linear Integrated Circuits</b>						
Sr No	UID NO	Name	ISE	MSE	ESE	TOTAL
			<b>20</b>	<b>20</b>	<b>60</b>	<b>100</b>
1	2016110054	SHEJWAL ADITYA VIVEK	13.5	3	24	<b>41</b>

<b>Micro-Architectures</b>						
Sr No	UID NO	Name	ISE	MSE	ESE	TOTAL
			<b>20</b>	<b>20</b>	<b>60</b>	<b>100</b>
1	2015110039	Ram G Pranav	10.31	3	AB	<b>14</b>

<b>Signals and Systems</b>						
Sr No	UID NO	Name	ISE	MSE	ESE	TOTAL
			<b>20</b>	<b>20</b>	<b>60</b>	<b>100</b>
1	2015110039	Ram G Pranav	9.9	0	AB	<b>10</b>
2	2016110054	SHEJWAL ADITYA VIVEK	17	1.5	33	<b>52</b>

<b>Electromagnetic Engineering</b>						
Sr No	UID NO	Name	ISE	MSE	ESE	TOTAL
			<b>20</b>	<b>20</b>	<b>60</b>	<b>100</b>
1	2015110039	Ram G Pranav	10	0	24	<b>34</b>

Controller of Examination

sd/-

Principal

sd/-