



BHARATIYA VIDYA BHAVAN'S
SARDAR PATEL INSTITUTE OF TECHNOLOGY

(Autonomous)

Munshi Nagar, Andheri (West), Mumbai – 400 058.

Date : 20/02/2019

ADMISSION NOTICE FOR Ph.D. PROGRAMME

IN

**(Computer Engg. Electronics Engg., Electronics & Telecomm.
and Master of Computer Application)**

Selected candidates are required to pay the fees and take admission from 21st February, 2019 to 26th February, 2019. Timings for payment of fees are 10.30 a.m. to 01:00 p.m. & 2:00 p.m. to 04.00 p.m.

The selected candidates should submit following original documents.

1. PET/Gate Score card
2. Degree Marksheet (All marksheet)
3. Post Graduate Degree Marksheet (All marksheet)
4. Post Graduate Degree Passing Certificate
5. Post Graduate Degree Leaving Certificate
6. Certificate of Indian Nationality
7. Migration Certificate [wherever applicable]
8. Caste Certificate [wherever applicable]
09. Caste Validity [wherever applicable]
10. Non - Creamy Layer Certificate [wherever applicable]
11. Two Passport size photos

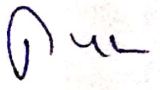
Date of Fees Payment : 21/02/2019 to 26/02/2019 Time of Fees Payment: 10.30 am to 4.00 pm

The fees structure is as below:	Amount of Fees
Tuition Fees	131120.00
Development Fees	17880.00
Library and Lab. Deposit (Refundable)	2000.00
University Share of Tuition Fees	5375.00
University Registration Fees	1000.00
University Registration Form Fees	25.00

Total	157400.00

Students are required to prepare a DD of Rs. 157400/- favouring "Principal, Sardar Patel Institute of Technology" payable at Mumbai.

Note : All the students are required to submit attested Xerox copies in three sets mentioned for above documents.


PRINCIPAL