

<b>BHARATIYA VIDYA BHAVAN'S</b>			
<b>SARDAR PATEL INSTITUTE OF TECHNOLOGY</b>			
<b>MUNSHI NAGAR ANDHERI (W), MUMBAI - 400 058.</b>			
<b>PROVISIONAL RESULT FOR EVEN SEM RE- EXAM JAN 2019</b>			
<b>Department - Electronics Engineering</b>			<b>SEM: VIII</b>
<b>SUBJECT/CODE-EXC801</b>		<b>Sub - CMOS VLSI Design</b>	<b>Date: 16-1-2019</b>
<b>Roll No/ Sr.No</b>	<b>UID NO</b>	<b>Last Name</b>	<b>Total</b>
			100
1	2014110030	KUMAR TUSHAR BALAJI JAYANTHI X	65

**COE**  
sd/-

**PRINCIPAL**  
sd/-