



BHARATIYA VIDYA BHAVAN'S
SARDAR PATEL INSTITUTE OF TECHNOLOGY

(Autonomous)

Munshi Nagar, Andheri (West), Mumbai – 400 058.

Date : 02/08/2018

ADMISSION NOTICE FOR Ph.D. PROGRAMME

IN

(Computer Engg. Electronics Engg. & Electronics & Telecomm.)

Selected candidates are required to pay the fees and take admission from 2nd August, 2018 to 08th August, 2018. Timings for payment of fees are 10.30 a.m. to 01:00 p.m. & 2:00 p.m. to 04.00 p.m.

The above candidates should submit following original documents.

1. PET/Gate Score card
2. Degree Marksheet (All marksheet)
3. Post Graduate Degree Marksheet (All marksheet)
4. Post Graduate Degree Passing Certificate
5. Post Graduate Degree Leaving Certificate
6. GATE score card [wherever applicable]
7. Certificate of Indian Nationality
8. Eligibility Certificate [wherever applicable]
9. Migration Certificate [wherever applicable]
10. Caste Certificate [wherever applicable]
11. Caste Validity [wherever applicable]
12. Two Passport size photos

Date of Fees Payment : 02/08/2018 to 08/08/2018 Time of Fees Payment: 10 am to 4 pm

The fees structure is as below:	Amount of Fees
Tuition Fees	131120.00
Development Fees	17880.00
Library and Lab. Deposit (Refundable)	2000.00
University Share of Tuition Fees	5375.00
University Registration Fees	1000.00
University Registration Form Fees	25.00
Total	157400.00

Students are required to prepare a DD of Rs. 157400/- favouring "Principal, Sardar Patel Institute of Technology" payable at Mumbai.

Note : All the students are required to submit attested Xerox copies in three sets mentioned for above documents.


PRINCIPAL