

Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to Mumbai University)

Programme – T.E. (Electronics Engineering) Semester – V

Make - up Semester Examination

(Revised Time Table)

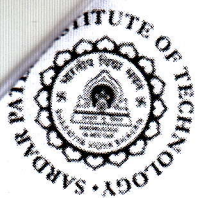
Days and Dates	Time	Paper
Friday, 15 th Dec 2017	10.00 A.M. – 01.00 P.M.	Microcontrollers and Applications
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Design with Linear Integrated Circuits
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Electromagnetic Engineering
Thursday, 21 st Dec 2017	10.00 A.M. – 01.00 P.M.	Signals and Systems
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Digital Communication

NOTE :

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2. Mobile Phones and other electronic gazets are prohibited in the examination hall.
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4. Change if any, in the time table shall be communicated on the college web site.


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PRINCIPAL



Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to Mumbai University)

Programme – T.E. (Computer Engineering) Semester – V

Make - up Semester Examination



(Revised Time Table)

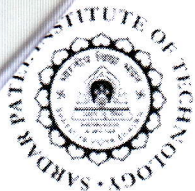
Days and Dates	Time	Paper
Friday, 15 th Dec 2017	10.00 A.M. – 01.00 P.M.	Microprocessor
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Operating System
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Structured and Object Oriented Analysis and Design
Thursday, 21 st Dec 2017	10.00 A.M. – 01.00 P.M.	Computer Networks

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Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
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Programme – T.E. (Information Technology) Semester – V

Make - up Semester Examination



(Revised Time Table)

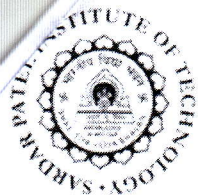
Days and Dates	Time	Paper
Friday, 15 th Dec 2017	10.00 A.M. – 01.00 P.M.	Computer Graphics And Virtual Reality
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Operating System
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Microcontroller and Embedded Systems
Thursday, 21 st Dec 2017	10.00 A.M. – 01.00 P.M.	Advanced Database Management Systems
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Open Source Technologies

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Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
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Programme – T.E. (Electronics & Telecommunication Engineering) Semester – V

Make - up Semester Examination

(Revised Time Table)

Days and Dates	Time	Paper
Friday, 15 th Dec 2017	10.00 A.M. – 01.00 P.M.	Microcontrollers and Applications
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Analog Communication
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Random Signal Analysis
Thursday, 21 st Dec 2017	10.00 A.M. – 01.00 P.M.	RF Modeling and Antennas
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Integrated Circuits

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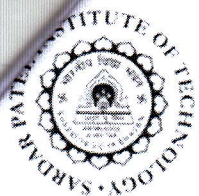


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Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
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Programme – B.E. (Electronics Engineering) Semester – VII

Make - up Semester Examination


(Revised Time Table)

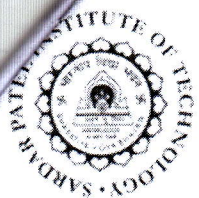
Days and Dates	Time	Paper
Monday, 18 th Dec 2017	10.00 A.M. – 01.00 P.M.	Embedded System Design
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	IC Technology
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Power Electronics-II
Friday, 22 nd Dec 2017	10.00 A.M. – 01.00 P.M.	Computer Communication Networks
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Elective-I Artificial Intelligence ASIC Verification Optical Fiber Communication

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Programme – B.E. (Computer Engineering) Semester – VII

Make - up Semester Examination



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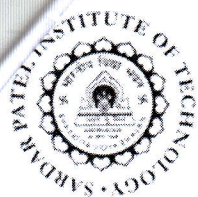
Days and Dates	Time	Paper
Monday, 18 th Dec 2017	10.00 A.M. – 01.00 P.M.	Digital Signal Processing
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Cryptography and System Security
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Artificial Intelligence
Tuesday, 22 nd Dec 2017	10.00 A.M. – 01.00 P.M.	Elective Advance Algorithms Computer Simulation and Modeling Image Processing Software Architecture Soft Computing ERP and Supply Chain Management

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Sardar Patel Institute of Technology

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Programme – B.E. (Information Technology) Semester – VII

Make - up Semester Examination

(Revised Time Table)

Days and Dates	Time	Paper
Monday, 18 th Dec 2017	10.00 A.M. – 01.00 P.M.	Software Project Management
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Cloud Computing
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Intelligent System
Friday, 22 nd Dec 2017	10.00 A.M. – 01.00 P.M.	Wireless Technology
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Elective-I Image processing Software Architecture E-Commerce & E-Business Multimedia Systems Usability Engineering Ubiquitous Computing

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Programme – B.E. (Electronics & Telecommunication Engineering) Semester – VII

Make - up Semester Examination

(Revised Time Table)

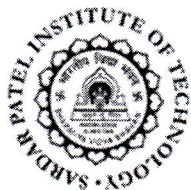
Days and Dates	Time	Paper
Monday, 18 th Dec 2017	10.00 A.M. – 01.00 P.M.	Image and Video Processing
Tuesday, 19 th Dec 2017	10.00 A.M. – 01.00 P.M.	Mobile Communication
Wednesday, 20 th Dec 2017	10.00 A.M. – 01.00 P.M.	Optical Communication and Networks
Friday, 22 nd Dec 2017	10.00 A.M. – 01.00 P.M.	Microwave and Radar Engineering
Tuesday, 26 th Dec 2017	10.00 A.M. – 01.00 P.M.	Elective : Data Compression and Encryption Statistical Signal Processing Neural Network and Fuzzy Logic Analog and Mixed Signal VLSI

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Programme – F.E. (All Branches) Semester - I

End – Semester Examination (Revised Time Table).

Days and Dates	Time	Paper
Wednesday, 6 th Dec 2017	10.00 A.M. – 01.00 P.M.	(BS13) Applied Chemistry – I
Friday, 8 th Dec 2017	10.00 A.M. – 01.00 P.M.	(BS11) Engineering Mathematics I
Saturday, 9 th Dec 2017	10.00 A.M. – 11.30 A.M.	(HSS11) Basic Communications Skills
Monday, 11 th Dec 2017	10.00 A.M. – 01.00 P.M.	(BS12) Applied Physics – I
Thursday, 14 th Dec 2017	10.00 A.M. – 01.00 P.M.	(ES11) Basic Electrical and Electronics Engineering / (ES14) Programming Methodology and Data Structures
Saturday, 16 th Dec 2017	10.00 A.M. – 01.00 P.M.	(ES12) Engineering Mechanics / (ES15) Engineering Graphics

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