

Bharatiya Vidya Bhavan's



Sardar Patel Institute of Technology

PRESENTS

Two-days faculty training on

**“Discrete Time Signal Processing using DSP”
(Hands-on Practice)**

29, 30th January 2016

Organized by

**Department of Electronics and
Telecommunication Engineering,
Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058**

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Vice Principal
Dr. Y. S. Rao

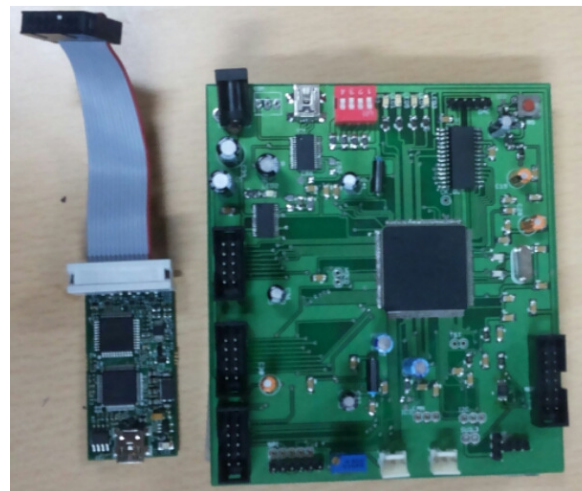
Principal
Dr. Prachi Gharpure

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To,

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Mumbai 400 058
Phone: 26707440/26708520 Extn. 380



From,

About us

In 1957, the Bharatiya Vidya Bhavan conceived the idea of establishing an engineering college in Mumbai. In 1995 Self Financed Engineering Course were added to it and it functioned as SPCE (Unaided-wing) conducting Electronics Engineering, Computer Engineering and Information Technology courses and Masters course in Electronics since 2005 till 2008. These courses have earned a great reputation in the field of engineering education, as well as industry. Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Unaided Wing from year 2005-2006 was established in its new building under the name and style of Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology and is affiliated to Mumbai University. Subsequently Electronics and Telecommunication course was started at graduate and post graduate level in the years 2006 and 2010 respectively. In addition to these programs Electronics and Telecommunication and Computer Engineering Departments have started Ph.D. program from 2012.

About the Workshop

This workshop covers the Programming of Texas Instruments DSPs using Code Composer Studio. The program is an ideal foundation for faculty to conduct practical's for Electronics, Electronics and Telecommunication and Computer Engineering programs for courses related to Discrete Time Signal Processing. The primary purpose of this workshop is to enable faculty to test theoretical concepts on practical hardware instead of simulation software's. It will also enhance the skill set of the faculty to train the students on developing DSP based Real-Time applications.

Expected outcomes:

After successful completion of this workshop, faculty will be able to configure Code Composer Studio (CCS) and use DSP to perform DFT, FFT, IDFT, IFFT, Convolution, Correlation and FIR filtering. This will motivate faculty and students to use DSPs for implementation of projects and research.

Resource Persons:

Dr. Y. S. Rao
Prof. K. T. Talele
Prof. Badri Narayan Mohapatra

Schedule:

Day1:

10 am to 1 pm: Lecture on Digital Signal Processors
2 pm to 5 pm: Introduction to CCS and Basic Programming

Day2:

10 am to 5.00 pm: Hands-on Practical on Discrete Time Signal Processing on DSP hardware

Venue:

R&D Lab, 310, 3th Floor, S.P.I.T.

Course Fees:

Faculty: Rs. 1000/-
Participants from Industry: Rs. 2000/-

Registration:

Cash can be paid on the date of workshop, Cheque or DD drawn in favor of "Principal S.P.I.T" payable at Mumbai. Confirm your registration by:

E-mail: ysrao@spit.ac.in

Mob: 9820962870

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29, 30th January 2016

Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution: _____

Address: _____

Email: _____

Tel: (O) _____ (Extn.) _____

(M) _____ (R) _____

Payment by Cash / Cheque / DD:

Chq. /DD No: _____ **Dated:** _____

Bank: _____

Amount Rs.: _____

Signature of the participant