



Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology
(AUTONOMOUS)

Munshi Nagar, Andheri (W), Mumbai – 400 058.

Date : 25.07.2018


List of Eligible Candidates Called for written test and Interview to Ph.D.
Programme in Electronics Engineering on

01/08/2018 at 3.00 p.m. Ground Floor, Room No. 005

Sr.No.	Name of the student	Category
1	Suryawanshi Santosh H.	OBC
2.	Goswami Siddhant Arun	OPEN
3.	Khandagle Kumar Gopinath	OPEN

All students are required to bring a statement of purpose and following original documents at the time of Interview.

1. Degree Marksheets (All Years / Semesters)
2. Degree Passing Certificate
3. Post Graduate Degree Marksheets (All Years / Semesters)
4. Post Graduate Degree Passing Certificate
5. PET/NET/Gate Score card (if applicable)
6. Certificate or any proof of Indian Nationality
7. Institution Leaving certificate (After qualifying examination)
8. Caste Certificate for SC / ST/ DT/NT/ OBC
9. Non - Creamy Layer Certificate
10. Caste Validity Certificate


PRINCIPAL
