

**BHARATIYA VIDYA BHAVAN'S  
SARDAR PATEL INSTITUTE OF TECHNOLOGY  
MUNSHI NAGAR ANDHERI (W), MUMBAI - 400 058.**

**EXAM - ESE\_EVEN\_SEM, MAY 2018  
PROVISIONAL RESULT AFTER GRIEVANCE**

<b>SEM-VI</b>		<b>Date: 29/05/2018</b>			<b>Sub -Basic VLSI Design</b>	
					<b>Sub Teacher :- Prof. N.A. Bhagat</b>	
<b>Branch - Electronics Engineering</b>					<b>BEFORE GRIEVANCE</b>	<b>AFTER GRIEVANCE</b>
					<b>OUT Of 60</b>	<b>OUT Of 60</b>
<b>Sr No</b>	<b>UID NO</b>	<b>Last Name</b>	<b>First Name</b>	<b>Fathers Name</b>	60	60
1	2015110003	Bhagat	Sahil	Sanjay	27	27
2	2015110004	Deshpande	Varun	Vinay	35	36
3	2015110005	Dongre	Isha	Mukund	36	40
4	2015110006	Ghadi	Apurv	Mangesh	49	50
5	2015110024	Mansoori	Shoaib	Salim	39	42
6	2015110028	Modi	Arpan	Kartik	42	42
7	2015110029	Moghe	Rajeev	Milind	38	39
8	2015110046	Sankhe	Saloni	Dipak	38	38
9	2015110047	Sawant	Divya	Ravindra	44	48
10	2015110049	Shah	Rajvee	Nilesh	45	47
11	2015110051	Shetty	Nitin	Dayanand	42	44
12	2015110057	Tripathi	Rahul	Rakesh	29	32
13	2016210064	Kodate	Rohit	Ashok	42	44

Controller of Examination

College Seal

PRINCIPAL  
sd/-