

**BHARATIYA VIDYA BHAVAN'S  
SARDAR PATEL INSTITUTE OF TECHNOLOGY  
MUNSHI NAGAR ANDHERI (W), MUMBAI - 400 058.**

**EXAM - ESE\_EVEN\_SEM, MAY 2018  
PROVISIONAL RESULT AFTER GRIEVANCE**

SEM-VIII	Date: 29/05/2018				Sub - CMOS VLSI Design	
					Sub Teacher :- Dr. S. S. Rathod	
<b>Branch - Electronics Engineering</b>					<b>BEFORE GRIEVANCE</b>	<b>AFTER GRIEVANCE</b>
					OUT of 60	OUT of 60
Roll No/Sr No	UID NO	Last Name	First Name	Fathers Name	60	60
1	2014110025	Kanojia	Vaibhav	Mukesh	<b>50</b>	<b>53</b>
2	2015210078	Shaikh	Shakila	Hamid	<b>16</b>	<b>17</b>

Controller of Examination

College Seal

PRINCIPAL  
sd/-