

# “Real Time Image Processing using DSP Processor” (Hands-on Practice)

16<sup>th</sup> & 17<sup>th</sup> March 2018

To,

Sardar Patel Institute of Technology,  
Munshi Nagar, Andheri (W),  
Mumbai 400 058

From,

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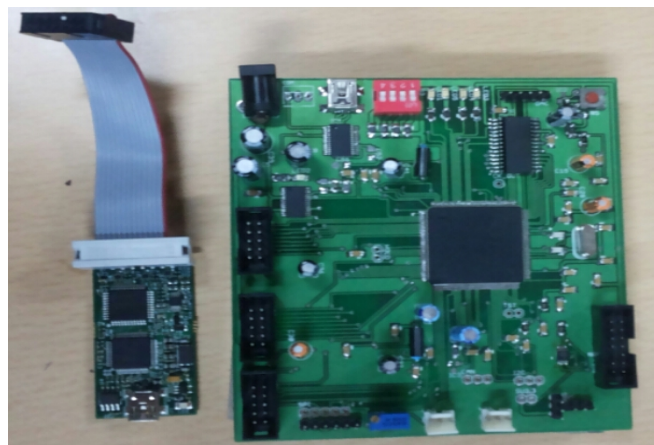
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## Schedule :

Day	Time	Topic
16/03/2018	10 to 1 pm	<b>Theory :</b> Introduction to Image Processing Color models Gray scale conversion Monochrome Image Image Thresholding and Inverse Thresholding Filters and application of filters in image processing
	2 to 5 pm	<b>Lab:</b> Texas TMS320F28335 DSP Processor Programming examples
17/03/2018	10 to 1 pm	<b>Theory:</b> Bit plane slicing Image Quantization Image sampling Edge detection Histogram equalization Contrast stretching
	2 to 5 pm	<b>Lab:</b> Texas TMS320F28335 DSP Processor Programming examples

Bharatiya Vidya Bhavan's



**Sardar Patel Institute of Technology**  
(Autonomous Institute)

**PRESENTS**

# “Real Time Image Processing using DSP Processor” (Hands-on Practice)

16<sup>th</sup> & 17<sup>th</sup> March 2018

Organized by

**Sardar Patel Institute of Technology,  
Munshi Nagar, Andheri (W),  
Mumbai 400 058**

Tel: 91-22-2670 8520, 26707440, 2628 7250

Fax No.: 91-22-26701422

[www.spit.ac.in](http://www.spit.ac.in)

**Vice Principal**  
Dr. Y. S. Rao

**Principal**  
Dr. Prachi Gharpure

## About us

In 1957, the Bharatiya Vidya Bhavan conceived the idea of establishing an engineering college in Mumbai. It was on the 19<sup>th</sup> August 1962 that there was a huge gathering at the Bhavan's Campus in Andheri to inaugurate Sardar Patel College of Engineering (SPCE). In 1995 Self Financed Engineering Course were added to it and it functioned as SPCE (Unaided-wing) conducting Electronics Engineering, Computer Engineering and Information Technology courses and Masters course in Electronics since 2005 till 2008. These courses have earned a great reputation in the field of engineering education, as well as industry. Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Unaided Wing from year 2005-2006 was established in its new building under the name and style of Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology and is affiliated to Mumbai University. Subsequently Electronics and Telecommunication course was started at graduate and post graduate level in the years 2006 and 2010 respectively. In addition to these programs Electronics, Electronics and Telecommunication, Computer Engineering and MCA Departments have started Ph.D. program from 2012. University of Mumbai has conferred Autonomous Status to S.P.I.T. for a period of five years from the academic year 2017-18 to 2021-22

## About the Program

This workshop covers the Programming of Texas Instruments Digital Signal Processors using Code Composer Studio. The program is an ideal foundation for faculty to conduct practical's for Electronics, Electronics and Telecommunication and Computer Engineering programs for courses related to Discrete Time Signal Processing and Image Processing. The primary purpose of this workshop is

to enable faculty to test theoretical concepts on practical hardware instead of simulation software's. It will also enhance the skill set of the faculty to train the students on developing DSP based Real-Time image processing applications.

### Expected outcomes:

After successful completion of this training program, faculty will be able to configure Code Composer Studio (CCS) and use Digital Signal Processor (DSP) for Real-time Image processing applications. This will motivate faculty to use DSPs for implementation of projects and research.

### Chief Patron:

**Dr. Prachi Gharpure**

### Resource Persons:

Dr. Y. S. Rao, Sardar Patel Institute of Technology, Mumbai

Dr. Bijith Marakarkandy, Thakur College of Engineering and Technology, Mumbai

Dr. J. H. Nirmal K.J.Somaiya College of Engineering, Mumbai

Prof. K. T. Talele Sardar Patel Institute of Technology, Mumbai

### Venue:

R&D Lab, 310, 3<sup>th</sup> Floor, S.P.I.T.

### Course Fees:

Faculty: Rs. 1200/- & Students: Rs. 800/-

### Registration:

DD drawn in favor of "Principal S.P.I.T" payable at Mumbai.

### Prof. Vijaya Kamble

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# "Real Time Image Processing using DSP Processor" (Hands-on Practice)

16<sup>th</sup> & 17<sup>th</sup> March 2018

Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Qualification: \_\_\_\_\_

Experience: \_\_\_\_\_

Institution: \_\_\_\_\_

Address: \_\_\_\_\_

Email: \_\_\_\_\_

Tel: (O) \_\_\_\_\_ (Extn.) \_\_\_\_\_

(M) \_\_\_\_\_ (R) \_\_\_\_\_

Payment by Cash / Cheque / DD:

Chq. /DD No: \_\_\_\_\_ Dated: \_\_\_\_\_

Bank: \_\_\_\_\_

Amount Rs.: \_\_\_\_\_

Signature of the participant :