Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India

(Autonomous Institute Affiliated to Mumbai University)

Programme – S.E. (Electronics Engineering) Semester – III

Make - up Semester Examination

(Revised Time Table)

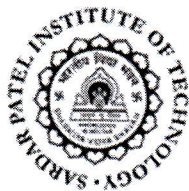| Days and Dates	Time	Paper
Monday, 18th Dec 2017	10.00 A.M. – 01.00 P.M.	Applied Mathematics I
Tuesday, 19th Dec 2017	10.00 A.M. – 01.00 P.M.	Analog Electronics – I
Wednesday, 20th Dec 2017	02.30 P.M. – 05.30 P.M.	Circuit Theory
Thursday, 21st Dec 2017	10.00 A.M. – 01.00 P.M.	Digital Circuits

NOTE :

1. The candidates appearing for the examination should report 15 minutes before the start of examination and occupied their seats as per seating arrangement.
2. Mobile Phones and other electronic gazets are prohibited in the examination hall.
3. Carry your ID card / Admission fee receipt at the time of Exam & any photo ID proof in original.
4. Change if any, in the time table shall be communicated on the college web site.

5. **If any overlap in above time table please informed to exam section Immediately**

C.O.E

PRINCIPAL

Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India

(Autonomous Institute Affiliated to Mumbai University)

Programme – S.E. (Computer Engineering) Semester – III

Make - up Semester Examination

(Revised Time Table)

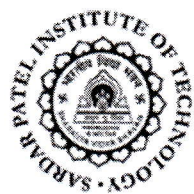| Days and Dates	Time	Paper
Monday, 18th Dec 2017	10.00 A.M. – 01.00 P.M.	Applied Mathematics I
Tuesday, 19th Dec 2017	10.00 A.M. – 01.00 P.M.	Advanced Data Structures
Wednesday, 20th Dec 2017	02.30 P.M. – 05.30 P.M.	Digital Logic Design and Analysis
Thursday, 21st Dec 2017	10.00 A.M. – 01.00 P.M.	Object Oriented Programming
Friday, 22nd Dec 2017	10.00 A.M. – 01.00 P.M.	Discrete Structures and Graph Theory

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C.O.E

PRINCIPAL

Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India

(Autonomous Institute Affiliated to Mumbai University)

Programme – S.E. (Electronics & Telecommunication Engineering) Semester – III

Make - up Semester Examination

(Revised Time Table)

Days and Dates	Time	Paper
Monday, 18th Dec 2017	10.00 A.M. – 01.00 P.M.	Applied Mathematics I
Tuesday, 19th Dec 2017	10.00 A.M. – 01.00 P.M.	Electronics Devices and Circuits
Wednesday, 20th Dec 2017	02.30 P.M. – 05.30 P.M.	Circuit Theory
Thursday, 21st Dec 2017	10.00 A.M. – 01.00 P.M.	Digital Circuits
Friday, 22nd Dec 2017	10.00 A.M. – 01.00 P.M.	Signals & Systems

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C.O.E

PRINCIPAL

Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India

(Autonomous Institute Affiliated to Mumbai University)

Programme – S.E. (Information Technology) Semester – III

Make - up Semester Examination

(Revised Time Table)

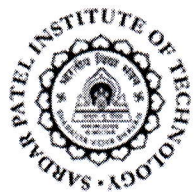| Days and Dates	Time	Paper
Monday, 18th Dec 2017	10.00 A.M. – 01.00 P.M.	Applied Mathematics I
Tuesday, 19th Dec 2017	10.00 A.M. – 01.00 P.M.	Advanced Data Structures
Wednesday, 20th Dec 2017	02.30 P.M. – 05.30 P.M.	Digital Logic Design and Analysis
Thursday, 21st Dec 2017	10.00 A.M. – 01.00 P.M.	Object Oriented Programming
Friday, 22nd Dec 2017	10.00 A.M. – 01.00 P.M.	Discrete Structures
Saturday, 23rd Dec 2017	10.00 A.M. – 01.00 P.M.	Information Theory and Coding

NOTE :

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PRINCIPAL

Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India

(Autonomous Institute Affiliated to Mumbai University)

Programme – S.Y. (Master of Computer Applications) Semester – III

Make-up Semester Examination

(Revised Time Table)

Days and Dates	Time	Paper
Tuesday, 19th Dec 2017	10.00 A.M. – 01.00 P.M.	Core and Advanced Java
Thursday, 21st Dec 2017	10.00 A.M. – 01.00 P.M.	Database Management System
Saturday, 23rd Dec 2017	10.00 A.M. – 01.00 P.M.	Operations Research
Tuesday, 26th Dec 2017	10.00 A.M. – 01.00 P.M.	Elective-I MCAE35 A Network Security MCAE35 B Artificial Intelligence MCAE35 C Management Information System MCAE35 D Computer Graphics and Image Processing MCAE35 E Service Oriented Architecture

NOTE :

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