



Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to Mumbai University)

Programme – M.Tech (Electronics & Telecommunication Engineering) Semester – I

End – Semester Examination

(Revised Time Table)

Days and Dates	Time	Paper
Wednesday, 6 th Dec 2017	10.00 A.M. – 01.00 P.M.	Advanced Digital Signal Processing
Friday, 8 th Dec 2017	10.00 A.M. – 01.00 P.M.	Modern Wireless Communication
Monday, 11 th Dec 2017	10.00 A.M. – 01.00 P.M.	Embedded System
Wednesday, 13 th Dec 2017	10.00 A.M. – 01.00 P.M.	Professional Elective-I Network Architecture and Design Computer Vision Machine Learning and Artificial Intelligence
Friday, 15 th Dec 2017	10.00 A.M. – 01.00 P.M.	Institute Elective-I Cyber Security and Laws

NOTE :

1. The candidates appearing for the examination should report 15 minutes before the start of examination and occupied their seats as per seating arrangement.
2. Mobile Phones and other electronic gazets are prohibited in the examination hall.
3. Carry your ID card / Admission fee receipt at the time of Exam & any photo ID proof in original.
4. Change if any, in the time table shall be communicated on the college web site.


C.O.E


PRINCIPAL