

Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology

(Autonomous)


Munshi Nagar, Andheri (W), Mumbai – 400 058.

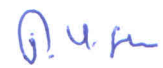
Date : 28.07.2017


**List of Students Selected for Ph.D. Programme in
Electronics Engg. for July - 2017**

Ph.D interviews were conducted on 28th July 2017. The selection criteria was based on written test and interview. Following candidates are selected against Open Category.

Sr. No.	Name of the Student	Category
1.	Mrs. Payal Hitesh Shah	OPEN
2.	Mrs. Vidya Rajesh Keshwani	OPEN
3.	Mrs. Rushali Ritesh Thakkar	OPEN
4.	Shri. Velankar Shrikant S.	OPEN
5.	Shri. Mahind Umesh S.	OPEN


Dr. Y. S. Rao
Vice Principal, Dean R. & D.


Dr. Prachi Gharpure
Principal


Dr. S. S. Rathod
HOD, ETRX


Dr. B. K. Lande
Expert