

ISTE approved one week faculty training on

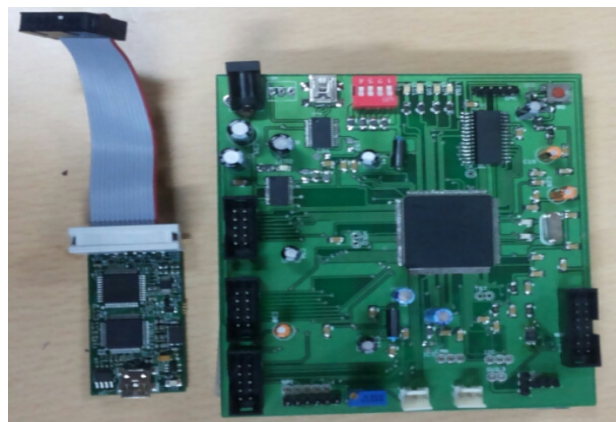
“Real Time Digital Signal Processing using DSP Processor” (Hands-on Practice)

13th June 2016 to 17th June 2016

To,

Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058
Phone: 26707440/26708520 Extn. 380

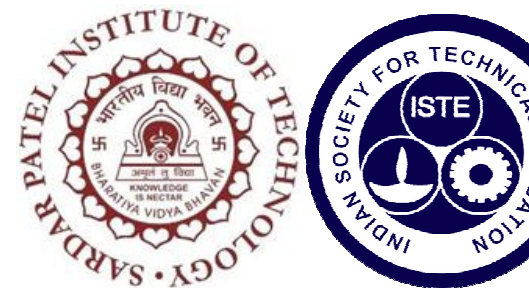
From,



Schedule :

Day	Time	Topic
13/06/2016 Dr. Y S Rao	10 to 1 pm	Real Time Signal Processing System Design; High performance computing
	2 to 5 pm	Code Composer Studio Simulation & Emulation
14/06/2016 Dr. Y S Rao	10 to 1 pm	Texas TMS320F28335 DSP Processor Design, Programming Model
	2 to 5 pm	DSP Processor Lab on GPIO, ADC, DAC, Timer, PWM
15/06/2016 Prof. K T Talele	10 to 1 pm	Linear Convolution ,Circular Convolution, DFT, IDFT , FFT, IFFT
	2 to 5 pm	DSP Processor Lab on DTSP
16/06/2016 Dr. J H Nirmal	10 to 1 pm	Linear Filtering design
	2 to 5 pm	DSP Processor Lab on Filter design
17/06/2016 Dr. R R Sawant	10 to 1 pm	DSP Applications in Speech, Image & Power Electronics
	2 to 5 pm	DSP Processor Lab on Real-time applications

Bharatiya Vidya Bhavan's



Sardar Patel Institute of Technology

PRESENTS

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“Real Time Digital Signal Processing using DSP Processor” (Hands-on Practice)

13th June 2016 to 17th June 2016

Organized by

**Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058**

Tel: 91-22-2670 8520, 26707440, 2628 72

Fax No.: 91-22-26701422

www.spit.ac.in

Vice Principal
Dr. Y. S. Rao

Principa
Dr. Prachi Ghar

About us

In 1957, the Bharatiya Vidya Bhavan conceived the idea of establishing an engineering college in Mumbai. In 1995 Self Financed Engineering Course were added to it and it functioned as SPCE (Unaided-wing) conducting Electronics Engineering, Computer Engineering and Information Technology courses and Masters course in Electronics since 2005 till 2008. These courses have earned a great reputation in the field of engineering education, as well as industry. Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Unaided Wing from year 2005-2006 was established in its new building under the name and style of Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology and is affiliated to Mumbai University. Subsequently Electronics and Telecommunication course was started at graduate and post graduate level in the years 2006 and 2010 respectively. In addition to these programs Electronics and Telecommunication and Computer Engineering Departments have started Ph.D. program from 2012.

About the Program

This workshop covers the Programming of Texas Instruments Digital Signal Processors using Code Composer Studio. The program is an ideal foundation for faculty to conduct practical's for Electronics, Electronics and Telecommunication and Computer Engineering programs for courses related to Discrete Time Signal Processing. The primary purpose of this workshop is to enable faculty to test theoretical concepts on practical hardware instead of simulation software's. It will also enhance the skill set of the faculty to train the students on developing DSP based Real-Time applications.

Expected outcomes:

After successful completion of this training program, faculty will be able to configure Code Composer Studio (CCS) and use DSP to perform DFT, FFT, IDFT, IFFT, Convolution, Correlation and FIR filtering. This will motivate faculty to use DSPs for implementation of projects and research.

Chief Patron:

Dr. Prachi Gharpure

Co-ordinators:

Dr. Y.S.Rao, Sardar Patel Institute of Technology, Mumbai

Prof. K.T.Talele, Sardar Patel Institute of Technology, Mumbai

Dr. J. H. Nirmal, K.J.Somaiya College of Engineering, Mumbai

Resource Persons:

Dr. Y. S. Rao, Sardar Patel Institute of Technology, Mumbai

Dr. R. R. Sawant, Vasantdada Patil College of Engineering, Mumbai

Dr. J. H. Nirmal, K.J.Somaiya College of Engineering, Mumbai

Prof. K. T. Talele, Sardar Patel Institute of Technology, Mumbai

Venue:

R&D Lab, 310, 3th Floor, S.P.I.T.

Course Fees:

Faculty: Rs. 3000/-

Participants from Industry: Rs. 5000/-

Registration:

DD drawn in favor of "Principal S.P.I.T"
payable at Mumbai.

Confirm your registration by: 5th June 2016

E-mail: ysrao@spit.ac.in

Mobile: 9820962870

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Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution: _____

Address: _____

Email: _____

Tel: (O) _____ (Extn.) _____

(M) _____ (R) _____

Payment by Cash / Cheque / DD:

Chq./DD No: _____ Dated: _____

Bank: _____

Amount Rs.: _____

Signature of the participant :