

Schedule of written test and interview for eligible candidates at Sardar Patel Institute of Technology

Post	Written Test DT./TIME	Interview	Time
PROFESSOR IN COMPUTER ENGG.	-	16.05.2016	10.00 am
PROFESSOR IN ELECTROINCS ENGG.	-	18.05.2016	10.00 am
PROFESSOR IN INFORMATION TECHNOLOGY	-	17.05.2016	10.00 am
PROFESSOR IN MASTER IN COMPUTER APPLICATION	-	17.05.2016	10.00 am
ASSOCIATE PROFESSOR IN ELECTRONICS ENGG.	-	18.05.2016	10.00 am
ASSOCIATE PROFESSOR IN ELECTROINCS AND TELECOMMUNICATION	-	19.05.2016	10.00 am
ASSISTANT PROFESSOR IN COMPUTER ENGG.	14.05.2016 at 10.30 am	16.05.2016	10.00 am
ASSISTANT PROFESSOR IN INFORMATION TECHNOLOGY	14.05.2016 at 12.00 noon	17.05.2016	10.00 am
ASSISTANT PROFESSOR IN ELECTRONICS ENGG.	14.05.2016 at 10.30 am	18.05.2016	12.00 noon
ASSISTANT PROFESSOR IN ELECTRONICS AND TELECOMMUNICATION	14.05.2016 at 12.00 noon	19.05.2016	10.00 am
ASSISTANT PROFESSOR IN MASTER OF COMPUTER APPLICATION	14.05.2016 at 2.00 pm	17.05.2016	11.00 am

Note:

1. Syllabus for written test will be put up separately on www.spit.ac.in
2. Candidates who qualify in the written test will attend the interview as per schedule.
3. The result of the written test will be displayed on the website 'spit.ac.in' and the college notice board on 14th May 2016 at 6 p.m.