

Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology

Munshi Nagar, Andheri (W), Mumbai – 400 058.


Date : 10.02.2015

**List of eligible candidates for Interview for Ph.D. Programme in
Electronics & Telecommunication Engineering for 2014-15**

Interview will be held on 21.02.2015 at 10.30 a.m. Conference Room of Sardar Patel Institute of Technology, Ground Floor, Room No. 005.

Sr.No.	Name of the student
1.	Paraye Milind Pandurang
2.	Bhavarthe Pramod Parashuram

Please refer notice for detailed schedule and documents required for attending Interview.


PRINCIPAL
