

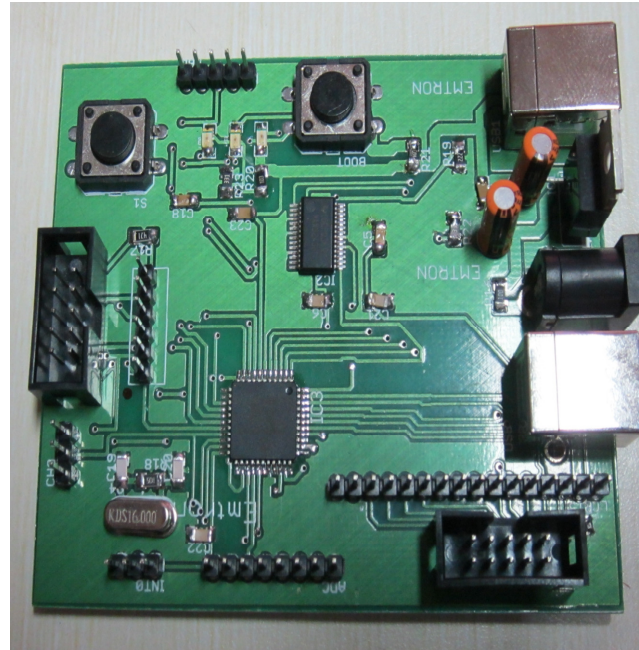
One-day faculty training on
**“PIC 18xx
Microcontroller IDE
Do it yourself”**

14th February 2015

To,

Department of Electronics and
Telecommunication Engineering,
Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058
Phone: 26707440/26708520 Extn. 380

From,



Bharatiya Vidya Bhavan's



Sardar Patel Institute of Technology

PRESENTS

One-day faculty training on

**“PIC 18xx
Microcontroller IDE
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14th February 2015

Organized by

**Department of Electronics and
Telecommunication Engineering,
Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058**

Tel: 91-22-2670 8520, 26707440, 2628 7250

Fax No.: 91-22-26701422

www.spit.ac.in

About us

In 1957, the Bharatiya Vidya Bhavan conceived the idea of establishing an engineering college in Mumbai. It was on the 19th August 1962 that there was a huge gathering at the Bhavan's Campus in Andheri to inaugurate Sardar Patel College of Engineering (SPCE). In 1995 Self Financed Engineering Course were added to it and it functioned as SPCE (Unaided-wing) conducting Electronics Engineering, Computer Engineering and Information Technology courses and Masters course in Electronics since 2005 till 2008. These courses have earned a great reputation in the field of engineering education, as well as industry. Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Unaided Wing from year 2005-2006 was established in its new building under the name and style of Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology and is affiliated to Mumbai University. Subsequently Electronics and Telecommunication course was started at graduate and post graduate level in the years 2006 and 2010 respectively. In addition to these programs Electronics and Telecommunication and Computer Engineering Departments have started Ph.D. program from 2012.

About the Workshop

This workshop mainly covers the Integrated Development Environment using PIC Microcontroller from Microchip. This course covers PIC design and provides hands on training on MPLAB Assembler, Compiler, Simulator and PIC18 IDE & HID boot loader. The program is an ideal foundation for faculty to conduct Electronics Workshop practicals and Embedded systems course.

Vice Principal

Dr. Y. S. Rao

Principal

Dr. Prachi Gharpure

Resource Persons:

Dr. Y. S. Rao

Schedule:

Theory: 10.00 am to 11.00 am
Demonstration: 11.00 am to 1.00 pm
Lunch: 1.00 pm to 1.30 pm
Hands-on Practical: 1.30 pm to 5.00 pm

Venue:

R&D Lab, 310, 3th Floor, S.P.I.T.

Course Fees:

Faculty: Rs. 500/-
Participants from Industry: Rs. 2000 /-

Registration:

Cash can be paid on the date of workshop, Cheque or DD drawn in favor of "Principal S.P.I.T" payable at Mumbai. Confirm your registration by:

E-mail: ysrao@spit.ac.in

Mob: 9820962870

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14th February 2015

Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution: _____

Address: _____

Email: _____

Tel: (O) _____ (Extn.) _____

(M) _____ (R) _____

Payment by Cash / Cheque / DD:

Chq. /DD No: _____ Dated: _____

Bank: _____

Amount Rs.: _____

Signature of the participant