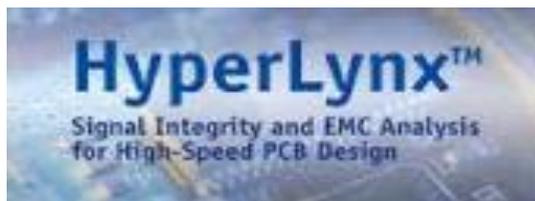One day workshop on

Mentor Graphics HyperLynx GHz Tool

26th August 2014

To,

Department of Electronics and
Telecommunication Engineering,
Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058
Phone: 26707440/26708520 Extn. 380

From,

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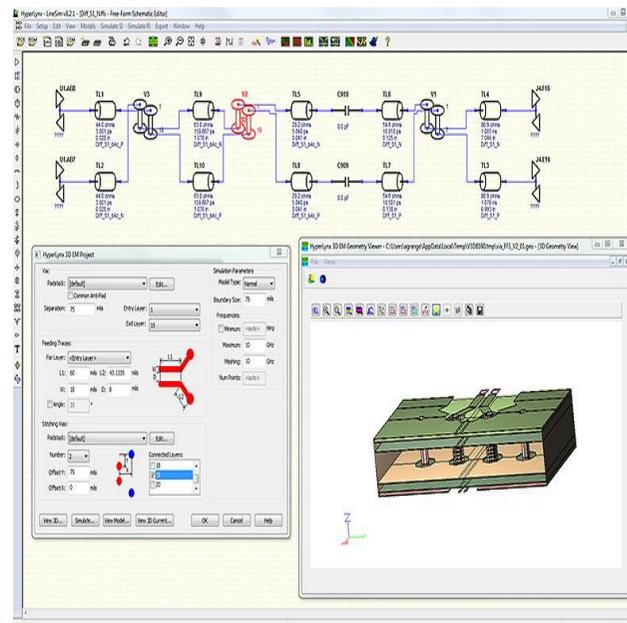_____

Coordinator:

1. Prof. Dattatray S. Sawant
   Dept. of Electronics and Telecom. Engg.,
   Phone: 26707440/26708520 Extn.386
   Mobile: 9769446447
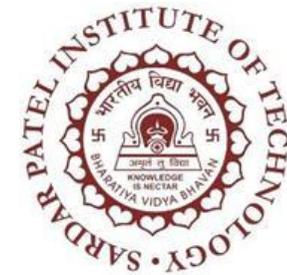   Email: dssawant1@gmail.com

Course Fees:

PG Students: Rs. 300 /-
Participants from Academic Institutes: Rs. 500/-
Participants from Industry: Rs. 2000 /-

Registration:

Cheque or DD drawn in the name of **"S.P.I.T.-F.E.T.S."** payable at Mumbai should reach on or before 24th Aug 2014 along with application form.

Vice Principal   Principal
Dr. Y. S. Rao   Dr. Prachi Gharpure

About us

In 1957, the Bharatiya Vidya Bhavan conceived the idea of establishing an engineering college in Mumbai. It was on the 19th August 1962 that there was a huge gathering at the Bhavan's Campus in Andheri to inaugurate Sardar Patel College of Engineering (SPCE). In 1995 Self Financed Engineering Course were added to it and it functioned as SPCE (Unaided-wing) conducting Electronics Engineering, Computer Engineering and Information Technology courses and Masters course in Electronics since 2005 till 2008. These courses have earned a great reputation in the field of engineering education, as well as industry. Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Unaided Wing from year 2005-2006 was established in its new building under the name and style of Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology and is affiliated to Mumbai University. Subsequently Electronics and Telecom. course was started at graduate and post graduate level in the years 2006 and 2010 respectively. In addition to these programs Electronics and Telecommunication and Computer Engineering Departments have started Ph.D. program from 2012.

About the Workshop

It has become important for the educators, practicing engineers and the students, to continuously update their knowledge and skills in design of high performance multi-Gbps channels. To verify theoretical concepts it is necessary to know the simulation tool very well. To achieve this goal, S.P.I.T. has invited engineers from Trident Techlabs to impart one day training on HyperLynx GHz tool. Trident Techlabs Pvt. Ltd. is a leading HyperLynx GHz tool provider and authorized partner for M/s Mentor Graphics Inc., USA. S.P.I.T. has 14 user licenses for mentor graphics tools on which actual hands-on sessions will be conducted.

Who should attend:

Faculties, Research Scholars, PG Students and Industry Professionals.

Resource Persons:

Mr. Lakshiminarayanan Rajamanickam
Senior PCB consultant (Industrial group)
Tridentechlabs Pvt Ltd. , Banglore.

Topics to be covered :

- Create and simulate LineSim free-form schematics
- Investigate termination strategies
- Investigate stack-up strategies
- Translate design databases
- Identify and debug SI and Crosstalk issues
- Simulate EMC and evaluate the results
- Assign models and component values
- Evaluate SI and Crosstalk issues
- Translate PCB layout databases
- Run BoardSim simulations interactively and in batch mode
- Run BoardSim for both single and multi-board projects
- Evaluate batch mode simulation reports to identify potential problems

Schedule:

Demo/Theory: 10.00 am to 1.00 pm
Lunch: 1.00 pm to 2.00 pm
Hands-on Practical: 2.00 pm to 5.00 pm

Venue:

CCN Lab, 503, 5th Floor, S.P.I.T.

One day workshop on

Mentor Graphics HyperLynx GHz Tool

26th August 2014

Name: _____

Designation: _____

Qualification:_____

Experience: _____

Institution: _____

Address: _____

_____

_____

Email: _____

Tel: (O) _____(Extn.) _____

(M)_____(R) _____

Payment by Cash / Cheque / DD:

Chq. /DD No: _____ Dated: _____

Bank: _____

Amount Rs.:_____

Signature of the participant