

BHARATIYA VIDYA BHAVAN'S
SARDAR PATEL INSTITUTE OF TECHNOLOGY
Munshi Nagar, Andheri (W), Mumbai – 400 058

Notice for Ph.D. Programme in Computer
Engineering and Electronics & Telecommunication
Engineering for 2014-15

All eligible candidates are informed to attend the interview for Admission to Ph.D. in Computer Engineering and Electronics & Telecommunication Engineering on 30.07.2014 at 02.00 p.m. in Conference Room of Sardar Patel Institute of Technology, Ground Floor, Room No. 005.

PRINCIPAL