

One-Week Graded ISTE-STTP Course on
**“Semiconductor Devices
and Circuits”**

8-12 July 2013

To,

Department of Electronics Engineering,
Sardar Patel Institute of Technology,
Munshi Nagar, Andheri (W),
Mumbai 400 058
Phone: 26707440/26708520 Extn. 350

From,

Course Convener:

Dr. Prachi Gharpure, Principal
Dr. Y.S. Rao, Vice-Principal

Course Coordinator:

Dr. Surendra Rathod

Organizing Committee:

1. Prof. P. V. Kasambe
2. Prof. Narendra Bhagat
3. Prof. G. T. Haldankar
4. Prof. Manisha Bansode
5. Prof. Payal Shah

Contact for registration:

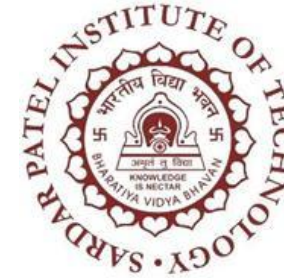
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**Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar,
Andheri (W), Mumbai 400 058**

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Bus from Andheri (W): 249/250/254/257/259



Bharatiya Vidya Bhavan's
**Sardar Patel Institute of Technology
&
ISTE**

Announces

One Week Graded ISTE-STTP Course on

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and Circuits”**

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www.spit.ac.in

About us

Sardar Patel Institute of Technology is under the umbrella of Bharatiya Vidya Bhavan & is academically affiliated to the University of Mumbai. It has been imparting technical education since 1996. Department of electronics engineering have earned a great reputation in the field of engineering education, as well as industry.

About the STTP

“Electronic Devices” and “Discrete Electronic Circuits” are the courses introduced in the Mumbai University revised syllabus in 2013. This STTP course is useful for faculties teaching these courses and conducting laboratory sessions of these courses in the second year of Electronics engineering.

Course Objectives:

1. To revise the concepts of energy band diagrams for different semiconductor devices.
2. To know the characteristics of semiconductor devices.
3. To revisit the concepts of discrete electronic circuits
4. To know the possibilities of conducting simulations and experiments in electronic devices and circuits.
5. To encourage interaction among subject teachers.

Venue:

Lectures: Conference Hall, 1st floor, S.P.I.T.
Practical: Room 305/303, 3rd Floor, S.P.I.T.

Course Fees:

Participants from Academic Institutes: Rs. 2,500/- only.
Participants from Industry: Rs. 3,000/- only.

Registration:

Cheque or D.D. drawn in the name of “PRINCIPAL, Sardar Patel Institute of Technology” payable at Mumbai should reach on or before 2nd July, 2013 along with application form.

Resource Persons:

1. Prof. M. B. Patil (IITB)
2. Dr. D. G. Borse (ACPCE)
3. Dr. V. N. Pawar (ACPCE)
4. Prof. B. R. Prabhu (KJSCE)
5. Dr. Sudhakar Mande (DBIT)
6. Dr. Ravishankar Dudhe (LTCOE)
7. Dr. D. V. Bhoir (CRCE)
8. Dr. Uday Pandit Khot (SFIT)
9. Prof. Poorva Waingankar (TCE)
10. Prof. Shikha Srivastav (SAKEC)
11. Prof. T. D. Biradar (DJSCE)
12. Prof. V. S. Padmakumar (GVAIET)
13. Prof. K. G. Sawarkar (RGIT)

Course Contents:

Date	Topics
8 th July 2013	Junction Analysis, Metal Semiconductor, Heterojun, Bipolar Devices
9 th July 2013	Field Effect (JFET, MOSFET, MESFET, HEMT), Optical and Power Devices
10 th July 2013	BJT and FET based circuit analysis
11 th July 2013	Multistage amplifiers, feedback amplifiers, oscillators
12 th July 2013	Differential amplifiers, power amplifiers
12 th July 2013 (2.00pm to 5.00pm)	Orientation Program for revised syllabus of 2 nd year Electronics Engineering subjects: EXC302, EXL301, EXC402 and EXL401

One -Week Graded ISTE-STTP Course on “Semiconductor Devices and Circuits” 8th July 2013 to 12th July 2013

Name: _____

Designation: _____

Qualification: _____

Experience: _____

Institution: _____

Address: _____

Email: _____

Tel: (O) _____ (Extn.) _____

(M) _____ (R) _____

Payment by Cash / Cheque / DD:

Chq. /DD No: _____ Dated: _____

Bank: _____

Amount Rs.: _____

(Cheque/DD, should be in the name of “Principal, Sardar Patel Institute of Technology., Mumbai payable at Mumbai)

Signature of the participant