

Tentative Time Table for ISTE-STTP on 'Semiconductor Devices and Circuits'

Date: 8th July to 12th July 2013

Date	Time	Topic	Speaker
8 th July 2013	9.00 am to 9.40 am	Registration & Inauguration	
8 th July 2013	9.45 am to 11.15 am	Junction Analysis	Prof. M. B. Patil (IITB)
8 th July 2013	11.30 am to 1.00 pm		
8 th July 2013	2.00 am to 3.30 pm	Bipolar Devices	Dr. D. V. Bhoir (CRCE)
8th July 2013	3.30 pm to 6.00 pm	<i>Lab on Electronic Devices</i>	
9 th July 2013	9.45 am to 11.15 am	Field Effect Devices	Dr. Ravishankar Dudhe (IGCOE)
9 th July 2013	11.30 am to 1.00 pm	Optical and Power Devices	Prof. Shikha Shrivastav (SAKEC)
9th July 2013	2.00 pm to 6.00 pm	<i>Lab on Electronic Devices</i>	
10 th July 2013	9.45 am to 11.15 am	Bipolar Device Based Circuit Analysis	Prof. Poorva Waingankar (TCE)
10 th July 2013	11.30 am to 1.00 pm	Field Effect Device Based Circuit Analysis	Dr. Sudhakar Mande (DBIT)
10th July 2013	2.00 pm to 6.00 pm	<i>Lab on Electronic Circuits</i>	
11 th July 2013	9.45 am to 11.15 am	Multistage and Frequency Analysis of Amplifiers	Prof. K. G. Sawarkar (RGIT)
11 th July 2013	11.30 am to 1.00 pm	Feedback Amplifiers	Prof. T. D. Biradar (DJSCE)
11 th July 2013	2.00 am to 3.30 pm	Oscillators	Dr. Uday Pandit Khot (SFIT)
11th July 2013	3.30 pm to 6.00 pm	<i>Lab on Electronic Circuits</i>	
12 th July 2013	9.45 am to 11.15 am	Differential Amplifiers	Prof. V. S. Padmakumar (GVAIET)
12 th July 2013	11.30 am to 1.00 pm	Power Amplifiers	Prof. B. R. Prabhu (KJSCE)
12th July 2013	2.00 pm to 6.00 pm	Orientation Program for revised syllabus of 2nd Year Electronics Engineering Subjects: EXC302, EXL301, EXC402 and EXL401	Dr. D. G. Borse, Chairman BoS Electronics Engineering Dr. V. N. Pawar (ACPCE) Dr. Ravishankar Dudhe (IGCOE)