Tentative Time Table for ISTE-STTP on 'Semiconductor Devices and Circuits'

Date: 8th July to 12th July 2013

Date	Time	Topic	Speaker
8 th July 2013	9.00 am to 9.40 am	Registration & Inauguration	
8 th July 2013	9.45 am to 11.15 am	Innation Analysis	Drof M. D. Dotil (HTD)
8 th July 2013	11.30 am to 1.00 pm	Junction Analysis	Prof. M. B. Patil (IITB)
8 th July 2013	2.00 am to 3.30 pm	Bipolar Devices	Dr. D. V. Bhoir (CRCE)
8 th July 2013	3.30 pm to 6.00 pm	Lab on Electronic Devices	
9 th July 2013	9.45 am to 11.15 am	Field Effect Devices	Dr. Ravishankar Dudhe (IGCOE)
9 th July 2013	11.30 am to 1.00 pm	Optical and Power Devices	Prof. Shikha Shrivastav (SAKEC)
9 th July 2013	2.00 pm to 6.00 pm	Lab on Electronic Devices	
10 th July 2013	9.45 am to 11.15 am	Bipolar Device Based Circuit	Prof. Poorva Waingankar (TCE)
		Analysis	
10 th July 2013	11.30 am to 1.00 pm	Field Effect Device Based	Dr. Sudhakar Mande (DBIT)
		Circuit Analysis	
10 th July 2013	2.00 pm to 6.00 pm	Lab on Electronic Circuits	
11 th July 2013	9.45 am to 11.15 am	Multistage and Frequency	Prof. K. G. Sawarkar (RGIT)
		Analysis of Amplifiers	
11 th July 2013	11.30 am to 1.00 pm	Feedback Amplifiers and	Prof. T. D. Biradar (DJSCE)
		Oscillators	
11 th July 2013	2.00 pm to 6.00 pm	Lab on Electronic Circuits	
12 th July 2013	9.45 am to 11.15 am	Differential Amplifiers	Prof. V. S. Padmakumar (GVAIET)
12 th July 2013	11.30 am to 1.00 pm	Power Amplifiers	Prof. B. R. Prabhu (KJSCE)
12 th July 2013	2.00 pm to 6.00 pm	Orientation Program for	Dr. D. G. Borse, Chairman BoS
		revised syllabus of 2 nd Year	Electronics Engineering
		Electronics Engineering	
		Subjects: EXC302, EXL301,	
		EXC402 and EXL401	