

1	Name of Teaching Staff :	Dr. Yerramreddy Srinivasa Rao			
2	Designation :	Professor & HOD			
3	Department :	Electronics & Telecommunication Engg.			
4	Date of Joining the Institution :	03/01/2000			
5	Qualification with Class / Grade :	UG	PG		Ph.D.
		B.E Electronics, Distinction	M.E. Electronics, Distinction		Electronics Systems, IIT-Bombay
6	Total Experience in years :	Teaching	Industry		Research
		19	02		07
7	Papers Published:	National		International	
		03		03	
8	Papers Presented in Conferences :	National		International	
		05		13	
9	Phd Guide? Give field and University : Dr. Mukul Chandorkar, Electrical Engg. Dept, IIT-Bombay, Powai, Mumbai.	Field: Electronics Systems, Power Electronics Power Systems		University: IIT-Bombay	

10	PhDs / Projects Guided :	Ph.Ds ---		Projects at Masters level 10	
11	Books Published / IPRs / Patents	----			
12	Professional Memberships :	ISTE, IEEE			

13	Consultancy Activities :	<ul style="list-style-type: none"> • D an Te ch no cra ft – Eth uo pia • Ind ra Pa cki ng Ma ch ine s - Ma lad • SN DT- U MI T - San tac ruz • Sa njiv ani Su gar Ind . - Ko par gao n • Shi rdi San sth an ICU - Shi 			
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		ch no log ies Lt d., An dh eri (W)			
14	Awards :	Honorable mention award by IEEE Computer Society, Washington D.C., USA, at the International Design Competition (CSIDC- 2002), for the project "Swift Doc".			
15	Grants fetched :	University of Mumbai – 35,000 IIT-Bombay – 3,50,000 Xilinx, USA – 4,50,000			
16	Interaction with Professional Institutions: NITIE, Powai ; UMIT, SNDT; Xavier, Mahim; SIES, Navi Mumbai; DJS COE, Vile Parle; NMIS, Vile Parle.	as a visiting faculty, technical committee expert, consultant, STTP courses.			

17	Subjects taught till today	Digital System Design, Digital Electronics, Industrial Electronics, Electronic Instrumentation, Microprocessors, Microcontrollers, Microprocessor System Design, Computer Organization, Advanced Computer Architecture, Mechatronics, Embedded Systems & Real Time Operating Systems, VLSI Design, Data Communication, Computer Networks.
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18. Con fere nce / Sem inar Atten ded / Pa per Publi shed Inter natio nal		
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18.1	Published	<ul style="list-style-type: none"> • Y. Srinivasa Rao and Mukul Chandorkar, 'Rapid Prototyping Tool for Electrical Load Emulation using Power Electronic Converters', Proceedings of the IEEE Symposium on Industrial Electronics and Applications, Kuala Lumpur, Malaysia, 4-6 October 2009. • Y. Srinivasa Rao and Mukul Chandorkar, 'Electrical Load Emulation using Power Electronic Converters', Proceedings of the IEEE Region 10 Conference, Hyderabad, India, 19-21 November 2008, ISBN:978-1-4244-2408-5. • Y. Srinivasa Rao and Mukul Chandorkar, 'Electrical Load Emulation using Optimal Feedback Control Technique', Proceedings of the IEEE International Conference on Industrial Technology, Gippsland, Australia, 9-13 February 2009, pp. 748-753, ISBN:978-1-4244-3507-4. • Ronak Gandhi, Y. S. Rao, Anish Raut and Ninad Sawe, "Green House Control Using Embedded Web-Server", SPIT-IEEE Colloquium 2008 and International Conference, 4-5 February 2008. • Y. S. Rao, Anish Raut and Ninad Sawe, "Real Time Vibration Analyzer Using Bluetooth Technology", SPIT-IEEE Colloquium 2008 and International Conference, 4-5 February 2008. • Sumeet Shetty, Meghana Korde and Y. S. Rao, "Wireless Sensored Networks", SPIT-IEEE Colloquium 2008 and International Conference, 4-5 February, 2008. • Nileshwar, K. Mhatre, N. Kuchroo, O. Mansata and Y. S. Rao, "Comparison of Fuzzy Control and PID Control in Embedded Systems" SPIT-IEEE Colloquium 2008 and International Conference, 4-5 February 2008.
18.2	Attended	07
	National	

18.3	Published	<ul style="list-style-type: none"> ● Y. Srinivasa Rao, Shivkumar Iyer, and Mukul Chandorkar, 'FPGA Implementation of a Phase Locked Loop for Digital Power Electronics', IEEE R8 International Conference on Computational Technologies in Electrical and Electronics Engineering SIBIRCON-2010, Irkutsk Listvyanka, Russia, 11-15 July 2010, pp. 665-670, ISBN: 978-1-4244-7625-1. ● Prashant Nair, Santosh Kumar, Srihari Narayanan and Y. Srinivasa Rao, 'Reconfigurable software for real-time emulation of electrical and electronics systems to aid measurement and testing' ICTM 2010 an IEEE calendar conference, Phuket, Thailand, 1-2 December 2010. ● Jatin Gangani, Amod Samant and Y. Srinivasa Rao 'Reconfigurable blocks for digital power electronics applications in FPGA', 26th Annual Applied Power Electronics Conference, Forth Worth, Texas, USA, March 6-10, 2011. ● Prashant Nair, Santosh Kumar and Y. Srinivasa Rao, ' Remote data acquisition and process monitoring on a low cost microcontroller using bluetooth & GSM technology' ICTM 2010 an IEEE calendar conference, Phuket, Thailand, 1-2 December 2010. ● Prashant Nair, Santosh Kumar and Y. Srinivasa Rao, 'Real Time Emulation of Electrical Systems for Rapid Prototyping in an Embedded Processor under a Multitasking Environment' India International Conference on Power Electronics IEEE IICPE 2010, New Delhi Jan 28-30 2011. ● Y. Srinivasa Rao and M. C. Chandorkar, 'Load Emulation with Power Electronic Converters', Proceedings of the National Power Electronic Conference, Indian Institute of Science, Bangalore, India, 17-19 December 2007.
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		<ul style="list-style-type: none"> Y. Srinivasa Rao and M. C. Chandorkar, 'Numerical Integration Methods for Digital Power Electronics and Electrical Drives,' CIEE-05 National Conference on Computational Intelligence for Electrical Engineering, pp. 20-24, 18-19, November 2005. <p>Microprocessor controlled copying attachment on shaper machine Proceedings of the 18th All India Manufacturing Technology Design and Research Conference, 9 - 13, December 21-23, 1998, IIT-Kharagpur .</p> <ul style="list-style-type: none"> PC Based ECG Analyzer and Transmitter, Y. S. Rao, R. P. Sood , Proceedings of the National Conference on Biomedical Engineering, pp 76-81, April 21-22, 2000 University of Roorkee. Handheld ECG Monitoring System, Y. S. Rao and R. P. Sood, Proceedings of the National Conference on Biomedical Engineering, pp 82-84, April 21-22, 2000 University of Roorkee.
18.4	Attended	10

18.5	Journal Papers	<ul style="list-style-type: none"> • Y. Srinivasa Rao and Mukul Chandorkar, 'Real-Time Electrical Load Emulator using Optimal Feedback Control Technique', accepted for publication IEEE Transactions on Industrial Electronics, April, 2010. • Y. Srinivasa Rao and Mukul Chandorkar, 'Electrical Load Emulator for Unbalanced Loads and with Power Regeneration', under review for IEEE Transactions on Industrial Electronics. • Y. S. Rao, Navan Savla, Nirav Shah, Amit Jardosh, and Kaushik Barat , "Swift Doc" Remote Monitoring of Heart abnormalities', IEEE Computer Society International Design Competition, CSIDC-2002, Washington D.C. • D. N. Kyatanavar, Y. S. Rao, and A N Tiwari 'Low Cost Video Digitizer', IE(I) Journal – ET, Vol 77, 99-87, March 1997. • DSP Add-On Card ,IETE Technical Review, D. N. Kyatanavar, Y. S. Rao, and A N Tiwari , pp 375-380, vol 16, No 3 & 4, May-August 1999. • PC Based ECG Analyzer, D. N. Kyatanavar, R P Sood and Y. S. Rao, IETE Technical Review , pp 33-40, vol 13, No-1, January-February 1996.
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18.6	STTP	<ul style="list-style-type: none"> • “Embedded Systems and VLSI Design”, 23-26 November 2010 ISTE-STTP course, Sinhgad Institute of Technology, Lonavala, Pune-410 401. • “Design with 8051 Microcontroller (Mechatronics)”, 19-20th March 2010, Mechanical Engg Dept, Sardar Patel College of Engineering, Anderi-W, Mumbai-58. • “Embedded Systems” 1st January, 2010, FRCE, Bandra-W, Mumbai • “Embedded Systems and Project Management”, 12th August, 2009, EXTC Dept. K. J. Somaiya Engineering College, Vidyavihar (W), Mumbai. • "Robotics: Basics to advance", 2nd June, 2009, EXTC Dept. Thadomal Shahani Engineering College, Bandra (W), Mumbai-50. • “Embedded Systems and RTOS”, SPIT-IEEE Colloquium and International conference, 4-5 Feb. 2008. • “Fundamentals of Circuit Simulation”, 28th August, 2008, Electronics Engg. Dept. SPIT, Mumbai. • “Real Time Systems”, Terna Engineering college, Nerul-W, STTP on “VLSI & Embedded systems” 9th July 2008. • “Software Tools for Electronics and Telecommunication Engg”, RGIT, 2008. • "DSP Applications" Electronics Engineering Dept, Sardar Patel College of Engineering, Munshinagar, Mumbai -58. • “Microcontrollers and VLSI devices”, June-2007, Physics Department Bhavan’s college. • "Emerging Micro electronics and VLSI Technologies for Communication Systems" 19th June-30th June 2006, Vidyavardini's College of Engineering & Technology, Vasai
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		<p>(W), Mumbai.</p> <ul style="list-style-type: none"> • “Advance Data base Management”, Don Bosco Institute of Technology, Kurla (W), Mumbai-70. • “Information Technology orientation for Engineering Teachers”, December 19 - 30, 2005, Department of Information Technology, D J S College of Engineering, Vile parle (W), Mumbai-56. • "Embedded DSP Design and Applications", June 05 - 16, 2006, Dept. of Information Technology St. Fransis Institute of Technology, Borivali (W), Mumbai-103. • “Recent Trends in Mechatronics”, May 8 – 12, 2006, Department of Mechanical Engineering , Sardar Patel College of Engineering, Munshinagar, Mumbai -58. • “C-Programming”, Mechanical Engineering Dept, Sardar Patel College of Engineering, Munshinagar, Mumbai -58. • “Advanced Applications in Electronics Engg.”, December 11 – 23, 2006, EXTC Dept. Thadomal Shahani Engineering College, Bandra (W), Mumbai-50. • “Microcontrollers and Embedded Systems”, November 27 – Dec.7, 2006, Department of Electronics Engg., D J S College of Engineering, Vile parle (W), Mumbai-56.
18.7	CEP IIT-Bombay	03
18.8	Industrial Training / Workshop: Microsoft-Bangalore, IBM-Bangalore, Symbian OS-Bangalore, TCS-Pune, WiPPRO-Pune, Mechatronics-Pune, Scilab IIT-Bombay, Xilinx University Program-S. P. I. T.	08
18.9	M.E. / M.Tech. IIT-Bombay, SPCE(UA)	15

18.10	Ph.D. IIT-Bombay	03
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