

1	Name of Teaching Staff :	Shah Payal Hitesh			
2	Designation :	Lecturer			
3	Department :	Electronics Engg.			
4	Date of Joining the Institution :	19-07-2006			
5	Qualification with Class / Grade :	UG	PG		Ph.D.
		B.E(Electrical)- First Class	M.E (AC R)- Disti nctio n		--
6	Total Experience in years :	Teaching	Indus try		Research
		4.5	--		--
7	Papers Published:	National		Interna tional	
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8	Papers Presented in Conferences:	National		Interna tional	
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9	Phd Guide? Give field and University :	Field: --		Univer sity: --	
10	PhDs / Projects Guided :	Ph.Ds --		Proje cts at Master s level --	
11	Books Published / IPRs / Patents:	--			
12	Professional Memberships :	--			
13	Consultancy Activities :	--			
14	Awards :	--			

15	Grants fetched :	--			
16	Interaction with Professional Institutions:	--			

17	Subjects taught till today	Basics of Electronics & Electrical Engg., Continuous Time Signals & Systems, Electrical Networks, Electronics Circuits Analysis & Design, Electronics & Electrical Measuring Instruments & Machines, CS, MFOC, DLDA
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18. Con fere nce / Sem inar Atten ded / Pa per Publi shed Inter natio nal		
18.1	Published	--
18.2	Attended	--
	National	
18.3	Published	--
18.4	Attended	--
18.5	Journal Papers	--
18.6	STTP	--
18.7	CEP	--
18.8	Industrial Training / Workshop	--
18.9	M.E. / M.Tech.	--
18.10	Ph.D.	--